LAN9220 / LAN9221 Mini Development Card

Asy 6527
PCB Revision B
Schematic Revision 1.1

Design Details
Board:
Asy 6527
Chip:
SMSC LAN9220 / LAN9221
Board Form Factor:
59.60mm x 58.42mm
Assembly:
56-Pin QFN

Revision History
Rev 1.0 to 1.1:
Page 2: Added note for default ADDR1 configuration

Circuit Diagrams utilizing SMSC Products Are Included As A Means Of Illustrating Typical Semiconductor Applications; Consequently Complete Information Sufficient For Construction Purposes Is Not Necessarily Given. The Information Has Been Carefully Checked And Is Believed To Be Entirely Reliable. However, No Responsibility Is Assumed For Inaccuracies. Furthermore, Such Information Does Not Convey To The Purchaser Of The Semiconductor Devices Described Any License Under The Patent Rights Of SMSC Or Others. SMSC Reserves The Right To Make Changes At Any Time In Order To Improve Design And Supply The Best Product Possible.

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Title Page
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NOTE: Pins 1 & 2 are used to detect the presence of an expansion card.

+3.3V

Local Bus Expansion Slot

Local Bus Test Points

NOTE:

ADDR1 is connected to RISER_RSVD0 (Pin 9 on Z1) by default. Remove R21 and populate R20 to connect ADDR1 to ADDR1_R (Pin 36 on Z1)
NOTE: Stuff JP1 for Auto-MDIIX Disabled
DNP JP1 for Auto-MDIIX Enabled

Decoupling Capacitors

MAC EEPROM (Optional)

IMPORTANT NOTE: Capacitors C22 – C25 are optional and are not present on the Mini Development Board. These capacitors are required for operation in an EMI constrained environment. When used, these capacitors must be placed close to the transformer.