LAN9218I or LAN9215 Mini Development Card

Assy 6511
PCB Revision B
Schematic Revision 1.0

Design Details
Board:
Assy 6511
Chip:
SMSC LAN9218I or SMSC LAN9215
Board Form Factor:
2.346" x 2.300"
Assembly:
100-Pin TQFP

Revision History
Rev 1.0:

Circuit Diagrams utilizing SMSC Products Are Included As A Means Of Illustrating Typical Semiconductor Applications: Consequently Complete Information Sufficient For Construction Purposes Is Not Necessarily Given. The Information Has Been Carefully Checked And Is Believed To Be Entirely Reliable. However, No Responsibility Is Assumed For Inaccuracies. Furthermore, Such Information Does Not Convey To The Purchaser Of The Semiconductor Devices Described Any License Under The Patent Rights Of SMSC Or Others. SMSC Reserves The Right To Make Changes At Any Time In Order To Improve Design And Supply The Best Product Possible.
NOTE:
Pin 1 is used to detect the presence of an expansion card.

Local Bus Expansion Slot

Local Bus Test Points

NOTE:
R320 does not go to the Local Bus Expansion Slot.
Table A: Device Configuration Straps

<table>
<thead>
<tr>
<th>SMSC Device</th>
<th>R13</th>
<th>R28</th>
<th>R22</th>
</tr>
</thead>
<tbody>
<tr>
<td>S315</td>
<td>DNP</td>
<td>DNP</td>
<td>Stuff</td>
</tr>
<tr>
<td>S318</td>
<td>DNP</td>
<td>GND</td>
<td>GND</td>
</tr>
</tbody>
</table>

1) R13, R28 & R22 in this schematic configure a LAN9218i device for 32 bit mode.
2) EEP_DATA pull-up resistor is not required for the S318 device because it is a 32 bit device only.
3) D24/MDIO pull-up resistor is not required for the S318 because MDI is not supported.

For the S315, the only function of this pull-up resistor is to give the system designer a mechanism to indicate the presence of an external PHY to a software application.

Decoupling Capacitors

MAC EEPROM (Optional)

NOTE:
A pull-down resistor on the EEPROM's chip select (CS) signal is recommended by many EEPROM manufacturers. Please refer to the EEPROM manufacturer's data sheet for further information.