The information contained herein is confidential and proprietary to SMSC, shall be used solely in accordance with the agreement pursuant to which it is provided, and shall not be reproduced or disclosed to others without the prior written consent of SMSC. Although the information is believed to be accurate, no responsibility is assumed for inaccuracies. SMSC reserves the right to make changes to this document and to specifications and product descriptions at any time without notice. Neither the provision of this information nor the sale of the described semiconductor devices conveys any licenses under any patent rights or other intellectual property rights of SMSC or others. The product may contain design defects or errors known as anomalies, including but not necessarily limited to any which may be identified in this document, which may cause the product to deviate from published specifications. SMSC products are not designed, intended, authorized or warranted for use in any life support or other application where product failure could cause or contribute to personal injury or severe property damage. Any and all such uses without prior written approval of an officer of SMSC will be fully at the risk of the customer. SMSC is a registered trademark of Standard Microsystems Corporation (“SMSC”).

SMSC DISCLAIMS AND EXCLUDES ANY AND ALL WARRANTIES, INCLUDING WITHOUT LIMITATION ANY AND ALL IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, TITLE, AND AGAINST INFRINGEMENT AND THE LIKE, AND ANY AND ALL WARRANTIES ARISING FROM ANY COURSE OF DEALING OR USAGE OF TRADE. IN NO EVENT SHALL SMSC BE LIABLE FOR ANY DIRECT, INCIDENTAL, INDIRECT, SPECIAL, PUNITIVE, OR CONSEQUENTIAL DAMAGES; OR FOR LOST DATA, PROFITS, SAVINGS OR REVENUES OF ANY KIND; REGARDLESS OF THE FORM OF ACTION, WHETHER BASED ON CONTRACT; TORT; NEGLIGENCE OF SMSC OR OTHERS; STRICT LIABILITY; BREACH OF WARRANTY; OR OTHERWISE; WHETHER OR NOT ANY REMEDY OF BUYER IS HELD TO HAVE FAILED OF ITS ESSENTIAL PURPOSE, AND WHETHER OR NOT SMSC HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.
Customer Evaluation Board for SMSC USB2602
Integrated USB2.0 Compatible 4-port HUB and Flash Media Controller
EVB-USB2602-CRB Rev B1
EVB-USB2602-QFP128 B

Features

- Operates from a single voltage (+5.0V, regulated) wall wart external power supply
- Low cost 2-Layer PCB design
- Serial EEPROM for configuration information – can be update from USB
- USB 2.0 HUB with three down-stream USB ports with individual port power control, over-current sense and green LED indicators
- Multi-TT enabled
- High-speed/Full-speed capable
- Supports these Media Types: Compact Flash, Micro Drive, MultiMediaCard, xD picture Card, Smart Media, Secure Digital, Memory Stick, High Speed Memory Stick, Memory Stick Pro
- Access to up to Four Memory Devices at the same time
- Less than 1mA VBUS current consumption

The EVB-USB2602-CRB features the integrated USB2602 USB 2.0 HUB and card reader. The board has one power connector to 5.0V external supply (J1: 2.1mm, tip -- POS), one upstream USB port on J2 and one USB down-stream port J3 on the rear side of the board. On the front side are two USB down-stream ports J6 and J7, and all card media sockets. This board demonstrates the first integrated USB HUB and card reader low-cost implementation in a single package.
Board Illustrations

Figure 1. Top side.

Figure 2. Bottom side.
EEPROM Configuration Settings

The board features an on-board EEPROM for configuration for VID/PID/DID of both Hub controller and card reader controller and serial number for the card reader. The USBDM windows application is used for viewing and changing the configuration stored in EEPROM. USBDM has several tabs to view and set different sections of the configuration. For more details see Software Release Notes. Figures 3 through 5 shows the default configuration for the EVB-USB2602-CRB.

Figure 3. USBDM Branding Tab – VID/PID and string descriptor configuration.
Figure 4. USBDM Configuration Tab – Card reader configuration.
Figure 5. USBDM Hub Tab – USB Hub configuration.