# Schematic Page

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POWER SUPPLY

3 V REGULATOR, 3A
(3V3 fixed when Rb=470E)

RESET Options

Note:
1. POR -> Reset to ASIC & SOC (Default)
2. RESET O/P from ASIC -> Reset to EX-PHY (PORT2) & SOC : Only Ethercat sku
3. RESET from SOC (GPIO/RST-O/P) -> Reset to ASIC
4. RESET from Push Button -> Reset to ASIC & SOC
Note:
OSCVSS need to connect to Chip gnd.
Note: Capacitors C28 through C31 are optional for EMI purposes and are not populated on the LAN9252 evaluation board. These capacitors are required for operation in an EMI constrained environment.

Note: Capacitors C33 through C36 are optional for EMI purposes and are not populated on the LAN9252 evaluation board. These capacitors are required for operation in an EMI constrained environment.
GPIO [0:2] & LED POL_Strap

Signals Functions
GPIO0 = LINKACTLED0/TDO/LEDPOL0/CHIP_MODE0
GPIO1 = LINKACTLED1/TDI/LEDPOL1/CHIP_MODE1
GPIO2 = RUNLED/LEDPOL2/E2PSIZE

Note: --To use GPIOs as LED
* Short 2-3 of both jumpers (ex. for GPIO0 short 2-3 of J4 & J7)

The LED is set as active low, CHIP_MODE0
The LED is set as active high.

Signal Name  Connector  Logic
GPIO0  0  J4,J7 (2-3)  Default
01  J4,J7 (1&2)
0  J4,J7 (2-3)  Default
1  J4,J7 (1&2)
0  J5,J8 (2-3)  Default
1  J5,J8 (1&2)
0  J6,J9 (2-3)  Default
1  J6,J9 (1&2)
0  J5,J8 (2-3)  Default
1  J5,J8 (1&2)
0  J6,J9 (2-3)  Default
1  J6,J9 (1&2)

I2C EEPROM

TH IC.
Different sizes can be mounted
I2C EEPROM Lower size
Below 16K(2K X 8) (24FC04)
I2C EEPROM Higher size
Above 16K(2K X 8) (24FC512)

FX_Los_Strap_1 & 2
FX_Mode_Strap_1 & 2

Strap Details

FX-LOS Strap_1 & 2

Note:
--To use GPIOs as LED
* Short 2-3 of both jumpers (ex. for GPIO0 short 2-3 of J4 & J7)
Note: Placement Instruction

Place the TPs in 100 mil distance from the respective IN_DIGIOx or OUT_DIGIOx PINS of J10 & J11

Placement should be such a way that, jumpers should be able to added between the test points and J10 or J11 connectors
Digital INPUTS

Default: All 8 Signals OFF

For Pull Down, move SW4 & SW5 to ON position

Default: All 8 Signals OFF

Digital OUTPUTS