The following errata apply to the following products (silicon revision):

- KSZ9031MNX (rev-A, A2)

<table>
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<th>Item #</th>
<th>Erratum (description of problem)</th>
<th>Solution / Workaround</th>
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or  
| 2      | The 125MHz reference clock (CLK125_NDO pin) output has duty cycle variation when the KSZ9031 links up in 1000Base-T Slave mode, resulting in wide variation on the falling clock edge. | Use only rising edge of CLK125_NDO output for PLL locking.  
or  
Set KSZ9031 to always link up in 1000Base-T Master mode by setting register 9h, bits [12:11] to ‘11’. |
| 3      | For Tri-color Dual-LED mode, the LED[2:1] pins’ toggle rate for transmit/receive activity indication is tied to the data rate.  
At low data rate, the LED[2:1] pins still toggle but the on/off toggling of the LED indicators is not visible to the eye. | Use external circuits to stretch the high assertions (LED indicator off periods) of the LED[2:1] outputs to make them visible to the eye.  
The LED[2:1] outputs are low when there is link-up and no activity.  
or  
Use Single-LED Mode |

For any questions about this errata and sample request, please contact your Micrel FAE or local Sales Representative.