High Side Current Monitor
8.0 to 450V
Voltage Gain of 1

Features
► Supply voltages from 8V to 450V
► Voltage output device
► Typical gain 1±1%
► Max $V_{SENSE}$ 500mV
► Fast rise and fall time, 700ns to 2.0µs
► Maximum quiescent current 50µA
► 5-Lead SOT-23 Package

Applications
► SMPS current monitor
► Battery current monitor
► Motor controls
► Telecom

General Description
The HV7800 high side current monitor IC transfers a high-side current measurement voltage to its ground referenced output with an accurate voltage gain of one. The measurement voltage typically originates at a current sense resistor which is located in a “high side” circuit, such as the positive supply line.

This monitor IC features a very wide input voltage range, high accuracy of transfer ratio, small size, low component count, low power consumption, ease of use, and low cost. Offline, battery and portable applications can be served equally well due to the wide input voltage range and the low quiescent current of the HV7800.

Typical Application Circuit

![Typical Application Circuit Diagram]
Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package Option</th>
<th>Packing</th>
</tr>
</thead>
<tbody>
<tr>
<td>HV7800K1-G</td>
<td>5-Lead SOT-23</td>
<td>2500/Reel</td>
</tr>
</tbody>
</table>

-G denotes a lead (Pb)-free / RoHS compliant package

Absolute Maximum Ratings

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>( V_{IN}, V_{LOAD} )</td>
<td>-0.5V to +460V</td>
</tr>
<tr>
<td>( V_{OUT} )</td>
<td>-0.5V to +10V</td>
</tr>
<tr>
<td>( V_{SENSE} )</td>
<td>-0.5V to +5.0V</td>
</tr>
<tr>
<td>( I_{LOAD} )</td>
<td>( \pm 10)mA</td>
</tr>
<tr>
<td>Operating ambient temperature</td>
<td>-40°C to +85°C</td>
</tr>
<tr>
<td>Operating junction temperature</td>
<td>-40°C to +125°C</td>
</tr>
<tr>
<td>Storage temperature</td>
<td>-65°C to +150°C</td>
</tr>
</tbody>
</table>

Absolute maximum ratings are those values beyond which damage to the device may occur. Functional operation under these conditions is not implied. Continuous operation of the device at the absolute rating level may affect device reliability. All voltages are referenced to device ground.

Notes:
1. Referenced to GND
2. \( V_{SENSE} = V_{IN} - V_{LOAD} \)

Pin Configuration

5-Lead SOT-23 (top view)

Typical Thermal Resistance

<table>
<thead>
<tr>
<th>Package</th>
<th>( \theta_{ja} )</th>
</tr>
</thead>
<tbody>
<tr>
<td>5-Lead SOT-23</td>
<td>253°C/W</td>
</tr>
</tbody>
</table>

Note:
Thermal testboard per JEDEC JESD51-7

Electrical Characteristics \( (T_a = 25°C \) unless otherwise specified, \( V_{IN} = 8\)V to 450V)

<table>
<thead>
<tr>
<th>Sym</th>
<th>Parameter</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
<th>Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Sym</td>
<td>Parameter</td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
<td>Units</td>
<td>Conditions</td>
</tr>
<tr>
<td>V_{IN}</td>
<td>Supply voltage</td>
<td>8.0</td>
<td>-</td>
<td>450</td>
<td>V</td>
<td>( \ast ) ---</td>
</tr>
<tr>
<td>I_Q</td>
<td>Quiescent supply current</td>
<td>-</td>
<td>-</td>
<td>50</td>
<td>( \mu ) A</td>
<td>( \ast ) ( V_{IN} = 8.0)V to 450V, ( V_{SENSE} = 0)mV</td>
</tr>
<tr>
<td>R_{OUT}</td>
<td>OUT pin output resistance</td>
<td>-</td>
<td>3.6</td>
<td>-</td>
<td>( \kappa ) O</td>
<td>---</td>
</tr>
<tr>
<td>V_{OUT}</td>
<td>Output voltage</td>
<td>0</td>
<td>-</td>
<td>15</td>
<td>mV</td>
<td>( \ast ) ( V_{SENSE} = 0)mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>79</td>
<td>-</td>
<td>121</td>
<td>mV</td>
<td>( \ast ) ( V_{SENSE} = 100)mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>177</td>
<td>-</td>
<td>223</td>
<td>mV</td>
<td>( \ast ) ( V_{SENSE} = 200)mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>470</td>
<td>-</td>
<td>530</td>
<td>mV</td>
<td>( \ast ) ( V_{SENSE} = 500)mV</td>
</tr>
</tbody>
</table>

Dynamic Characteristics

<table>
<thead>
<tr>
<th>Sym</th>
<th>Parameter</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
<th>Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>t_{Rise}</td>
<td>Output rise time, 10% to 90%</td>
<td>-</td>
<td>0.7</td>
<td>-</td>
<td>( \mu ) s</td>
<td>( \ast ) ( V_{SENSE} ) step 5.0mV to 500mV</td>
</tr>
<tr>
<td>t_{Fall}</td>
<td>Output fall time, 90% to 10%</td>
<td>-</td>
<td>0.7</td>
<td>2.0</td>
<td>( \mu ) s</td>
<td>( \ast ) ( V_{SENSE} ) step 500mV to 0mV</td>
</tr>
</tbody>
</table>

* Values apply over the full temperature range
Typical Performance Characteristics

- **$V_{OUT}$**
  - $V_{SENSE} = 1500mV$
  - $V_{SENSE} = 1000mV$
  - $V_{SENSE} = 800mV$
  - $V_{SENSE} = 500mV$
  - $V_{SENSE} = 200mV$
  - $V_{SENSE} = 100mV$

- **$\Delta V_{OUT}$**
  - $V_{SENSE} = 500mV$
  - **Curve represents level of $V_{IN}$ which causes a 1% drop in $V_{OUT}$**

- **Minimum $V_{IN}$**
  - $V_{SENSE} = 500mV$

- **Maximum $V_{OUT}$**
  - $V_{SENSE} = V_{OUT} + 250mV$

$T_{AMB} = 25°C$
Typical Performance Characteristics (cont.)

**I_{IN}**

- Top Curve: $V_{IN} = 400V$
- Bottom Curve: $V_{IN} = 10V$
- $V_{SENSE} = 0mV$

**Frequency**

- $V_{OUT}/V_{SENSE}$ (Gain)
- $V_{IN}/V_{OUT}$ (PSRR)

**Step Response**

- (from pos 10mV)
- (from neg 10mV)
**Application Information**

**General**
The HV7800 high side current monitor IC features accurate current sensing, small size, low component count, low power consumption, exceptional input voltage range, ease of use and low cost.

The part typically performs the measurement of line or load current for overcurrent protection, metering or current regulation.

High side current sensing, as opposed to ground referenced or low side current sensing, is desirable or required when:

- The current to be measured does not flow in a circuit associated with ground.
- The measurement at ground level can lead to ambiguity due to changes in the grounding arrangement during field use.
- Introduction of a sense resistor in the system ground is undesirable due to issues with safety, EMI, or signal degradation caused by common impedance coupling.

**Principle of Operation**
The operational amplifier and MOSFET force the voltage across $R_A$ to track $V_{\text{SENSE}}$ within the limit of the offset voltage of the opamp, i.e. $V_{RA} = V_{\text{SENSE}}$.

The current through $R_A$ returns to ground through $R_B$. $R_A$ and $R_B$ are integrated, exhibiting tight matching and excellent tracking. By design, $R_A$ and $R_B$ have the same resistance. Consequently, $V_{RA}$ is equal to $V_{RB}$, resulting in a voltage gain of 1.

**OUT Pin Loading Effects**
Note that the OUT pin has a typical output resistance of 3.6kΩ. Loading the output causes the voltage gain to drop and rise/fall time to increase.

For example, assuming an output resistance of 3.6kΩ, the load resistance should exceed 3.6MΩ in order to limit the drop in gain to 1 part in 1000.

Again assuming an output resistance of 3.6kΩ, capacitive loading of 30pF results in a response pole with a time constant of 100ns, not yet high enough to materially affect the output rise and fall time (about 700ns).

**Sense Resistor Considerations**
Choose a sense resistor that will not exceed 500mV during normal operating conditions. Limit the power dissipation in the sense resistor to whatever is practical; a high sense voltage benefits accuracy, but increases power dissipation.

Consider the use of Kelvin connections for applications where considerable voltage drops may occur in the PCB traces that carry the current to be measured to the sense resistor. A layout pattern that minimizes voltage across the sense lines is shown below.

Choose a low inductance type sense resistor if preservation of bandwidth is important. The use of Kelvin connections helps by excluding the inductive voltage drop across the traces leading to the sense resistor. The inductive voltage drop may be substantial when operating at high frequencies.
A trace or component inductance of just 10nH contributes an impedance of 6.2mΩ at 100kHz, which constitutes a 6% error when using a 100mΩ sense resistor.

**Transient Protection**

Add a protection resistor ($R_p$) in series with the LOAD pin if $V_{SENSE}$ can exceed 5.0V in a positive sense or 600mV in a negative sense, whether in a steady state or in transient conditions.

A large $V_{SENSE}$ may occur during system startup or shutdown due to the charging and discharging of bulk storage capacitors. $V_{SENSE}$ may be large due to fault conditions, such as a short circuit condition, or a broken or missing sense resistor.

An internal 5.0V Zener diode with a current rating of 10mA protects the sense amplifier inputs. The block diagram shows the orientation of this diode. The Zener diode provides clamping at 5.0V for a positive $V_{SENSE}$ and at 600mV for a negative $V_{SENSE}$.

Under worst case conditions, limit the Zener current to 10mA. A 100kΩ resistor limits the Zener diode current to 4.5mA when $V_{SENSE}$ is 450V, whether positive or negative. Note that the protection resistor may affect the bandwidth. The resistor forms a RC network with the trace and pin capacitance at the LOAD pin. A capacitance of 5.0pF results in a time constant of 500ns.

The protection resistor may cause an offset due to bias current at the LOAD input. Under worst case bias current (1.0nA), a 100kΩ protection resistor could cause an offset of 100µV or 0.2% of full scale. Note that the bias current is nominally zero as the LOAD is a high impedance CMOS input.

<table>
<thead>
<tr>
<th>Pin #</th>
<th>Pin Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>LOAD</td>
<td>Sense amplifier input. High impedance input with Zener diode protection. Add an external protection resistor in series with LOAD if $V_{SENSE}$ exceeds the range of -600mV to +5V.</td>
</tr>
<tr>
<td>2</td>
<td>NC</td>
<td>No connect. This pin must be left floating for proper operation.</td>
</tr>
<tr>
<td>3</td>
<td>IN</td>
<td>Sense amplifier input and supply.</td>
</tr>
<tr>
<td>4</td>
<td>GND</td>
<td>Supply return.</td>
</tr>
<tr>
<td>5</td>
<td>OUT</td>
<td>Output with a nominal output resistance of 3.6kΩ. Preservation of accuracy may require an external buffer amplifier to prevent excessive loading.</td>
</tr>
</tbody>
</table>
Supertex inc. does not recommend the use of its products in life support applications, and will not knowingly sell them for use in such applications unless it receives an adequate "product liability indemnification insurance agreement." Supertex inc. does not assume responsibility for use of devices described, and limits its liability to the replacement of the devices determined defective due to workmanship. No responsibility is assumed for possible omissions and inaccuracies. Circuitry and specifications are subject to change without notice. For the latest product specifications refer to the Supertex inc. website: http://www.supertex.com

©2013 Supertex inc. All rights reserved. Unauthorized use or reproduction is prohibited.

5-Lead SOT-23 Package Outline (K1)
2.90x1.60mm body, 1.45mm height (max), 0.95mm pitch

Note:
1. A Pin 1 identifier must be located in the index area indicated. The Pin 1 identifier can be: a molded mark/identifier; an embedded metal marker; or a printed indicator.

<table>
<thead>
<tr>
<th>Symbol</th>
<th>A</th>
<th>A1</th>
<th>A2</th>
<th>b</th>
<th>D</th>
<th>E</th>
<th>E1</th>
<th>e</th>
<th>e1</th>
<th>L</th>
<th>L1</th>
<th>L2</th>
<th>θ</th>
<th>θ1</th>
</tr>
</thead>
<tbody>
<tr>
<td>MIN</td>
<td>0.90*</td>
<td>0.00</td>
<td>0.90</td>
<td>0.30</td>
<td>2.75*</td>
<td>2.60*</td>
<td>1.45*</td>
<td>0.95</td>
<td>0.95</td>
<td>0.30</td>
<td>0.60</td>
<td>0.25</td>
<td>BSC</td>
<td>BSC</td>
</tr>
<tr>
<td>NOM</td>
<td>-</td>
<td>-</td>
<td>1.15</td>
<td>-</td>
<td>2.90</td>
<td>2.80</td>
<td>1.60</td>
<td>0.95</td>
<td>0.95</td>
<td>0.30</td>
<td>0.60</td>
<td>0.25</td>
<td>REF</td>
<td>BSC</td>
</tr>
<tr>
<td>MAX</td>
<td>1.45</td>
<td>0.15</td>
<td>1.30</td>
<td>0.50</td>
<td>3.05*</td>
<td>3.00*</td>
<td>1.75*</td>
<td>0.95</td>
<td>0.95</td>
<td>0.30</td>
<td>0.60</td>
<td>0.25</td>
<td>BSC</td>
<td>BSC</td>
</tr>
</tbody>
</table>

* This dimension is not specified in the JEDEC drawing.

Drawings not to scale.
Supertex Doc. #: DSPD-5SOT23K1, Version A041309.

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to http://www.supertex.com/packaging.html.)