Resistor bridge for input power supply level check by application
Input power supply level accepted are in the range 0V up to 10V
Levels must fit the 3.3V maximum reference of the ADC

TP4 = (10/(10+22)) \( \times \) 0.3125
TP1

Host VBUS generation with Overcurrent Detection in output

3.3V Regulator

AVCC3 intends to provide a clean 3.3V power supply for analog blocks
VDDIO intends to provide a clean 3.3V power supply for MCU buffers
VDDIN intends to provide a clean 3.3V power supply for MCU internal regulator
VCC3 intends to provide a clean 3.3V power supply for peripherals
Audio filter

- R55: 0R
- R56: 270k
- R57: 0R
- R58: 0R
- R59: 1k
- R60: 3.9k
- C61: 22uF/6V
- C62: 2.2n
- J19: N.M