Circuit Diagrams utilizing SMSC Products Are Included As A Means Of Illustrating Typical Semiconductor Applications: Consequently Complete Information Sufficient For Construction Purposes Is Not Necessarily Given. The Information Has Been Carefully Checked And Is Believed To Be Entirely Reliable. However, No Responsibility Is Assumed For Inaccuracies. Furthermore, Such Information Does Not Convey To The Purchaser Of The Semiconductor Devices Described Any License Under The Patent Rights Of SMSC Or Others. SMSC Reserves The Right To Make Changes At Any Time In Order To Improve Design And Supply The Best Product Possible.
NOTE: Populate R6, R8 and R14 according to table below.

NOTE: Pin 7 is a no-connect (NC) for LAN9500A, but may be connected to VDD33A for backward compatibility with LAN9500 designs.

NOTE: Place R2-R5 near LAN9500A. Place R6 and C1 near transformer.

NOTE: For R6, see note, top left on this page.

NOTE: For R8, see note, top left on this page.

NOTE: In an EMI-constrained environment, populate these capacitors. These components must be placed close to the transformer.