Circuit diagrams utilizing SMSC products are included as a means of illustrating typical semiconductor applications: consequently complete information sufficient for construction purposes is not necessarily given. The information has been carefully checked and is believed to be entirely reliable. However, no responsibility is assumed for inaccuracies. Furthermore, such information does not convey to the purchaser of the semiconductor devices described any license under the patent rights of SMSC or others. SMSC reserves the right to make changes at any time in order to improve design and supply the best product possible.
NOTE: FFPC_SEL can be driven dynamically and is not pulled internally to either state. This pin has the same timing as the Address bus and can be driven by upper address bits not used by the LAN9218I. Refer to data sheet.

NOTE: IN = Auto-MDI Functionality Enabled
OUT = Auto-MDI Functionality Disabled

NOTE: EE_DIO is the D32/D16# Strap
1 - 2 = 32 Bit Mode
3 - 5 = 16 Bit Mode

NOTE: C18 must be low ESR capacitors. Ceramic is recommended.