MCP9700/9700A
MCP9701/9701A

Low-Power Linear Active Thermistor™ ICs

Features

• Tiny Analog Temperature Sensor
• Available Packages: SC-70-5, TO-92-3
• Wide Temperature Measurement Range:
  - -40°C to +125°C
• Accuracy:
  - ±2°C (max.), 0°C to +70°C (MCP9700A/9701A)
  - ±4°C (max.), 0°C to +70°C (MCP9700/9701)
• Optimized for Analog-to-Digital Converters (ADCs):
  - 10.0 mV/°C (typ.) MCP9700/9700A
  - 19.5 mV/°C (typ.) MCP9701/9701A
• Wide Operating Voltage Range:
  - VDD = 2.3V to 5.5V MCP9700/9700A
  - VDD = 3.1V to 5.5V MCP9701/9701A
• Low Operating Current: 6 µA (typ.)
• Optimized to Drive Large Capacitive Loads

Typical Applications

• Hard Disk Drives and Other PC Peripherals
• Entertainment Systems
• Home Appliance
• Office Equipment
• Battery Packs and Portable Equipment
• General Purpose Temperature Monitoring

Typical Application Circuit

The MCP9700/9700A and MCP9701/9701A family of Linear Active Thermistor™ Intergrated Circuit (IC) is an analog temperature sensor that converts temperature to analog voltage. It's a low-cost, low-power sensor with an accuracy of ±2°C from 0°C to +70°C (MCP9700A/9701A) ±4°C from 0°C to +70°C (MCP9700/9701) while consuming 6 µA (typ.) of operating current.

Unlike resistive sensors (such as thermistors), the Linear Active Thermistor IC does not require an additional signal-conditioning circuit. Therefore, the biasing circuit development overhead for thermistor solutions can be avoided by implementing this low-cost device. The voltage output pin (VOUT) can be directly connected to the ADC input of a microcontroller. The MCP9700/9700A and MCP9701/9701A temperature coefficients are scaled to provide a 1°C/bit resolution for an 8-bit ADC with a reference voltage of 2.5V and 5V, respectively.

The MCP9700/9700A and MCP9701/9701A provide a low-cost solution for applications that require measurement of a relative change of temperature. When measuring relative change in temperature from +25°C, an accuracy of ±1°C (typ.) can be realized from 0°C to +70°C. This accuracy can also be achieved by applying system calibration at +25°C.

In addition, this family is immune to the effects of parasitic capacitance and can drive large capacitive loads. This provides Printed Circuit Board (PCB) layout design flexibility by enabling the device to be remotely located from the microcontroller. Adding some capacitance at the output also helps the output transient response by reducing overshoots or undershoots. However, capacitive load is not required for sensor output stability.

Package Type
1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings †

- **VDD**: 6.0V
- Storage temperature: -65°C to +150°C
- Ambient Temp. with Power Applied: -40°C to +125°C
- Junction Temperature (TJ): 150°C
- ESD Protection On All Pins (HBM:MM): (4 kV:200V)
- Latch-Up Current at Each Pin: ±200 mA

†Notice: Stresses above those listed under “Maximum Ratings” may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operational listings of this specification is not implied. Exposure to maximum rating conditions for extended periods may affect device reliability.

DC ELECTRICAL CHARACTERISTICS

### Electrical Specifications:

- **MCP9700/9700A**: VDD = 2.3V to 5.5V, GND = Ground, TA = -40°C to +125°C and No load.
- **MCP9701/9701A**: VDD = 3.1V to 5.5V, GND = Ground, TA = -10°C to +125°C and No load.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Sym</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
<th>Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power Supply</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Operating Voltage Range</td>
<td>VDD</td>
<td>2.3</td>
<td>—</td>
<td>5.5</td>
<td>V</td>
<td>MCP9700/9700A</td>
</tr>
<tr>
<td></td>
<td>VDD</td>
<td>3.1</td>
<td>—</td>
<td>5.5</td>
<td>V</td>
<td>MCP9701/9701A</td>
</tr>
<tr>
<td>Operating Current</td>
<td>IDD</td>
<td>—</td>
<td>6</td>
<td>12</td>
<td>μA</td>
<td></td>
</tr>
<tr>
<td>Power Supply Rejection</td>
<td>ΔC/ΔVDD</td>
<td>—</td>
<td>0.1</td>
<td>—</td>
<td>°C/V</td>
<td></td>
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<tr>
<td>Sensor Accuracy (Notes 1, 2)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>T_A = +25°C</td>
<td>T_ACY</td>
<td>—</td>
<td>±1</td>
<td>—</td>
<td>°C</td>
<td>MCP9700A/9701A</td>
</tr>
<tr>
<td>T_A = 0°C to +70°C</td>
<td>T_ACY</td>
<td>-2.0</td>
<td>—</td>
<td>+2.0</td>
<td>°C</td>
<td>MCP9700A</td>
</tr>
<tr>
<td>T_A = -40°C to +125°C</td>
<td>T_ACY</td>
<td>-2.0</td>
<td>—</td>
<td>+4.0</td>
<td>°C</td>
<td>MCP9701A</td>
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<tr>
<td>T_A = 0°C to +70°C</td>
<td>T_ACY</td>
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<td>—</td>
<td>+4.0</td>
<td>°C</td>
<td>MCP9700/9701</td>
</tr>
<tr>
<td>T_A = -40°C to +125°C</td>
<td>T_ACY</td>
<td>-4.0</td>
<td>—</td>
<td>+6.0</td>
<td>°C</td>
<td>MCP9700</td>
</tr>
<tr>
<td>T_A = -10°C to +125°C</td>
<td>T_ACY</td>
<td>-4.0</td>
<td>—</td>
<td>+6.0</td>
<td>°C</td>
<td>MCP9701</td>
</tr>
<tr>
<td>Sensor Output</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Output Voltage, T_A = 0°C</td>
<td>V_0°C</td>
<td>—</td>
<td>500</td>
<td>—</td>
<td>mV</td>
<td>MCP9700/9700A</td>
</tr>
<tr>
<td>Output Voltage, T_A = 0°C</td>
<td>V_0°C</td>
<td>—</td>
<td>400</td>
<td>—</td>
<td>mV</td>
<td>MCP9701/9701A</td>
</tr>
<tr>
<td>Temperature Coefficient</td>
<td>T_C</td>
<td>10.0</td>
<td>—</td>
<td>—</td>
<td>mV/°C</td>
<td>MCP9700/9700A</td>
</tr>
<tr>
<td></td>
<td>T_C</td>
<td>19.5</td>
<td>—</td>
<td>—</td>
<td>mV/°C</td>
<td>MCP9701/9701A</td>
</tr>
<tr>
<td>Output Non-linearity</td>
<td>V_ONL</td>
<td>—</td>
<td>±0.5</td>
<td>—</td>
<td>°C</td>
<td>T_A = 0°C to +70°C (Note 2)</td>
</tr>
<tr>
<td>Output Current</td>
<td>I_OUT</td>
<td>—</td>
<td>—</td>
<td>100</td>
<td>μA</td>
<td></td>
</tr>
<tr>
<td>Output Impedance</td>
<td>Z_OUT</td>
<td>—</td>
<td>20</td>
<td>—</td>
<td>Ω</td>
<td>I_OUT = 100 μA, f = 500 Hz</td>
</tr>
<tr>
<td>Output Load Regulation</td>
<td>ΔV_OUT/ΔI_OUT</td>
<td>—</td>
<td>1</td>
<td>—</td>
<td>Ω</td>
<td>T_A = 0°C to +70°C, I_OUT = 100 μA</td>
</tr>
</tbody>
</table>

**Note 1:** The MCP9700/9700A family accuracy is tested with VDD = 3.3V, while the MCP9701/9701A accuracy is tested with VDD = 5.0V.

**Note 2:** The MCP9700/9700A and MCP9701/9701A family is characterized using the first-order or linear equation, as shown in Equation 4-2.

**Note 3:** The MCP9700/9700A and MCP9701/9701A family is characterized and production tested with a capacitive load of 1000 pF.

**Note 4:** SC-70-5 package thermal response with 1x1 inch, dual-sided copper clad, TO-92-3 package thermal response without PCB (leaded).
DC ELECTRICAL CHARACTERISTICS (CONTINUED)

Electrical Specifications: Unless otherwise indicated:
MCP9700/9700A: \( V_{DD} = 2.3\text{V to 5.5V}, GND = \text{Ground}, T_A = -40\text{°C to } +125\text{°C and No load.} \)
MCP9701/9701A: \( V_{DD} = 3.1\text{V to 5.5V}, GND = \text{Ground}, T_A = -10\text{°C to } +125\text{°C and No load.} \)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Sym</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Unit</th>
<th>Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Turn-on Time</td>
<td>( t_{ON} )</td>
<td>—</td>
<td>800</td>
<td>—</td>
<td>µs</td>
<td></td>
</tr>
<tr>
<td>Typical Load Capacitance (Note 3)</td>
<td>( C_{LOAD} )</td>
<td>—</td>
<td>—</td>
<td>1000</td>
<td>pF</td>
<td></td>
</tr>
<tr>
<td>SC-70 Thermal Response to 63%</td>
<td>( t_{RES} )</td>
<td>—</td>
<td>1.3</td>
<td>—</td>
<td>s</td>
<td>30°C (Air) to +125°C (Fluid Bath) (Note 4)</td>
</tr>
<tr>
<td>TO-92 Thermal Response to 63%</td>
<td>( t_{RES} )</td>
<td>—</td>
<td>1.65</td>
<td>—</td>
<td>s</td>
<td></td>
</tr>
</tbody>
</table>

Note 1: The MCP9700/9700A family accuracy is tested with \( V_{DD} = 3.3\text{V}, \) while the MCP9701/9701A accuracy is tested with \( V_{DD} = 5.0\text{V}. \)
2: The MCP9700/9700A and MCP9701/9701A family is characterized using the first-order or linear equation, as shown in Equation 4-2.
3: The MCP9700/9700A and MCP9701/9701A family is characterized and production tested with a capacitive load of 1000 pF.
4: SC-70-5 package thermal response with 1x1 inch, dual-sided copper clad, TO-92-3 package thermal response without PCB (leaded).

TEMPERATURE CHARACTERISTICS

Electrical Specifications: Unless otherwise indicated:
MCP9700/9700A: \( V_{DD} = 2.3\text{V to 5.5V}, GND = \text{Ground}, T_A = -40\text{°C to } +125\text{°C and No load.} \)
MCP9701/9701A: \( V_{DD} = 3.1\text{V to 5.5V}, GND = \text{Ground}, T_A = -10\text{°C to } +125\text{°C and No load.} \)

<table>
<thead>
<tr>
<th>Parameters</th>
<th>Sym</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
<th>Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Specified Temperature Range</td>
<td>( T_A )</td>
<td>-40</td>
<td>—</td>
<td>+125</td>
<td>°C</td>
<td>MCP9700/9700A (Note)</td>
</tr>
<tr>
<td>Specified Temperature Range</td>
<td>( T_A )</td>
<td>-10</td>
<td>—</td>
<td>+125</td>
<td>°C</td>
<td>MCP9701/9701A (Note)</td>
</tr>
<tr>
<td>Operating Temperature Range</td>
<td>( T_A )</td>
<td>-40</td>
<td>—</td>
<td>+125</td>
<td>°C</td>
<td></td>
</tr>
<tr>
<td>Storage Temperature Range</td>
<td>( T_A )</td>
<td>-65</td>
<td>—</td>
<td>+150</td>
<td>°C</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Thermal Package Resistances</th>
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<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Thermal Resistance, SC-70-5</td>
<td>( \theta_{JA} )</td>
<td>—</td>
<td>331</td>
<td>—</td>
<td>°C/W</td>
</tr>
<tr>
<td>Thermal Resistance, TO-92-3</td>
<td>( \theta_{JA} )</td>
<td>—</td>
<td>131.9</td>
<td>—</td>
<td>°C/W</td>
</tr>
</tbody>
</table>

Note: Operation in this range must not cause \( T_J \) to exceed Maximum Junction Temperature (+150°C).
2.0 TYPICAL PERFORMANCE CURVES

Note: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.

Note: Unless otherwise indicated, **MCP9700/9700A**: $V_{DD} = 2.3V$ to $5.5V$; **MCP9701/9701A**: $V_{DD} = 3.1V$ to $5.5V$; GND = Ground, $C_{bypass} = 0.1 \mu F$.

**FIGURE 2-1:** Accuracy vs. Ambient Temperature (MCP9700A/9701A).

**FIGURE 2-2:** Accuracy vs. Ambient Temperature, with $V_{DD}$.

**FIGURE 2-3:** Supply Current vs. Temperature.

**FIGURE 2-4:** Accuracy vs. Ambient Temperature (MCP9700/9701).

**FIGURE 2-5:** Changes in Accuracy vs. Ambient Temperature (Due to Load).

**FIGURE 2-6:** Load Regulation vs. Ambient Temperature.
Note: Unless otherwise indicated, MCP9700/9700A: V\textsubscript{DD} = 2.3V to 5.5V; MCP9701/9701A: V\textsubscript{DD} = 3.1V to 5.5V; GND = Ground, C\textsubscript{bypass} = 0.1 µF.

**FIGURE 2-7:** Output Voltage at 0°C (MCP9700/9700A).

**FIGURE 2-8:** Occurrences vs. Temperature Coefficient (MCP9700/9700A).

**FIGURE 2-9:** Power Supply Rejection ($\Delta$C/$\Delta$V\textsubscript{DD}) vs. Ambient Temperature.

**FIGURE 2-10:** Output Voltage at 0°C (MCP9701/9701A).

**FIGURE 2-11:** Occurrences vs. Temperature Coefficient (MCP9701/9701A).

**FIGURE 2-12:** Power Supply Rejection ($\Delta$C/$\Delta$V\textsubscript{DD}) vs. Temperature.
MCP9700/9700A and MCP9701/9701A

Note: Unless otherwise indicated, **MCP9700/9700A**: $V_{DD} = 2.3\text{V to } 5.5\text{V};$ **MCP9701/9701A**: $V_{DD} = 3.1\text{V to } 5.5\text{V};$ GND = Ground, $C_{\text{bypass}} = 0.1 \mu\text{F}$.

**FIGURE 2-13:** Output Voltage vs. Power Supply.

**FIGURE 2-14:** Output vs. Settling Time to step $V_{DD}$.

**FIGURE 2-15:** Thermal Response (Air to Fluid Bath).

**FIGURE 2-16:** Output Voltage vs. Ambient Temperature.

**FIGURE 2-17:** Output vs. Settling Time to Ramp $V_{DD}$.

**FIGURE 2-18:** Output Impedance vs. Frequency.
3.0 PIN DESCRIPTIONS

The descriptions of the pins are listed Table 3-1.

<table>
<thead>
<tr>
<th>Pin No. SC-70</th>
<th>Pin No. TO-92</th>
<th>Symbol</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>—</td>
<td>NC</td>
<td>No Connect</td>
</tr>
<tr>
<td>2</td>
<td>3</td>
<td>GND</td>
<td>Power Ground Pin</td>
</tr>
<tr>
<td>3</td>
<td>2</td>
<td>(V_{\text{OUT}})</td>
<td>Output Voltage Pin</td>
</tr>
<tr>
<td>4</td>
<td>1</td>
<td>(V_{\text{DD}})</td>
<td>Power Supply Input</td>
</tr>
<tr>
<td>5</td>
<td>—</td>
<td>NC</td>
<td>No Connect</td>
</tr>
</tbody>
</table>

3.1 Power Ground Pin (GND)

GND is the system ground pin.

3.2 Output Voltage Pin (\(V_{\text{OUT}}\))

The sensor output can be measured at \(V_{\text{OUT}}\). The voltage range over the operating temperature range for the MCP9700/9700A is 100 mV to 1.75V and for the MCP9701/9701A, 200 mV to 3V.

3.3 Power Supply Input (\(V_{\text{DD}}\))

The operating voltage as specified in the “DC Electrical Characteristics” table is applied to \(V_{\text{DD}}\). 
4.0 APPLICATIONS INFORMATION

The Linear Active Thermistor™ IC uses an internal diode to measure temperature. The diode electrical characteristics have a temperature coefficient that provides a change in voltage based on the relative ambient temperature from -40°C to 125°C. The change in voltage is scaled to a temperature coefficient of 10.0 mV/°C (typ.) for the MCP9700/9700A and 19.5 mV/°C (typ.) for the MCP9701/9701A. The output voltage at 0°C is also scaled to 500 mV (typ.) and 400 mV (typ.) for the MCP9700/9700A and MCP9701/9701A, respectively. This linear scale is described in the first-order transfer function shown in Equation 4-1.

EQUATION 4-1: SENSOR TRANSFER FUNCTION

\[ V_{OUT} = T_C \cdot T_A + V_{0°C} \]

Where:
- \( T_A \) = Ambient Temperature
- \( V_{OUT} \) = Sensor Output Voltage
- \( V_{0°C} \) = Sensor Output Voltage at 0°C
- \( T_C \) = Temperature Coefficient

4.1 Improving Accuracy

The MCP9700/9700A and MCP9701/9701A accuracy can be improved by performing a system calibration at a specific temperature. For example, calibrating the system at +25°C ambient improves the measurement accuracy to ±0.5°C (typ.) from 0°C to +70°C, as shown in Figure 4-1. Therefore, when measuring relative temperature change, this family measures temperature with higher accuracy.

For higher accuracy using a sensor compensation technique, refer to AN1001 “IC Temperature Sensor Accuracy Compensation with a PICmicro® Microcontroller” (DS01001). The application note shows that if the MCP9700 is compensated in addition to room temperature calibration, the sensor accuracy can be improved to ±0.5°C (typ.) accuracy over the operating temperature (Figure 4-2).

4.2 Shutdown Using Microcontroller I/O Pin

The MCP9700/9700A and MCP9701/9701A family of low operating current of 6 µA (typ.) makes it ideal for battery-powered applications. However, for applications that require tighter current budget, this device can be powered using a microcontroller Input/Output (I/O) pin. The I/O pin can be toggled to shut down the device. In such applications, the microcontroller internal digital switching noise is emitted to the MCP9700/9700A and MCP9701/9701A as power supply noise. This switching noise compromises measurement accuracy. Therefore, a decoupling capacitor and series resistor will be necessary to filter out the system noise.

4.3 Layout Considerations

The MCP9700/9700A and MCP9701/9701A family does not require any additional components to operate. However, it is recommended that a decoupling capacitor of 0.1 µF to 1 µF be used between the \( V_{DD} \) and GND pins. In high-noise applications, connect the power supply voltage to the \( V_{DD} \) pin using a 200Ω resistor with a 1 µF decoupling capacitor. A high frequency ceramic capacitor is recommended. It is necessary for the capacitor to be located as close as possible to the \( V_{DD} \) and GND pins in order to provide effective noise protection. In addition, avoid tracing digital lines in close proximity to the sensor.
4.4 Thermal Considerations

The MCP9700/9700A and MCP9701/9701A family measures temperature by monitoring the voltage of a diode located in the die. A low-impedance thermal path between the die and the PCB is provided by the pins. Therefore, the sensor effectively monitors the temperature of the PCB. However, the thermal path for the ambient air is not as efficient because the plastic device package functions as a thermal insulator from the die. However, the plastic device package insulates the die and restricts device thermal response. This limitation applies to plastic-packaged silicon temperature sensors. If the application requires measuring ambient air, the PCB needs to be designed with proper thermal conduction to the sensor pins.

The MCP9700/9700A and MCP9701/9701A is designed to source/sink 100 µA (max.). The power dissipation due to the output current is relatively insignificant. The effect of the output current can be described using Equation 4-2.

**EQUATION 4-2:** EFFECT OF SELF-HEATING

\[
T_J - T_A = \theta_{JA}(V_{DD}I_{DD}) + (V_{DD} - V_{OUT})I_{OUT}
\]

Where:

- \(T_J\) = Junction Temperature
- \(T_A\) = Ambient Temperature
- \(\theta_{JA}\) = Package Thermal Resistance 
  (331°C/W)
- \(V_{OUT}\) = Sensor Output Voltage
- \(I_{OUT}\) = Sensor Output Current
- \(I_{DD}\) = Operating Current
- \(V_{DD}\) = Operating Voltage

At \(T_A = +25\text{°C}\) (\(V_{OUT} = 0.75V\)) and maximum specification of \(I_{DD} = 12\mu A\), \(V_{DD} = 5.5V\) and \(I_{OUT} = +100\mu A\), the self-heating due to power dissipation \((T_J - T_A)\) is 0.179°C.
5.0 PACKAGING INFORMATION

5.1 Package Marking Information

5-Lead SC-70 (MCP9700/MCP9700A)

Example:

<table>
<thead>
<tr>
<th>Device</th>
<th>Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>MCP9700/9700A</td>
<td>AUN</td>
</tr>
<tr>
<td>MCP9701/9701A</td>
<td>AVN</td>
</tr>
</tbody>
</table>

Note: Applies to 5-Lead SC-70.

Legend:

- XX...X Customer-specific information
- Y Year code (last digit of calendar year)
- YY Year code (last 2 digits of calendar year)
- WW Week code (week of January 1 is week '01')
- NNN Alphanumeric traceability code
- 3e Pb-free JEDEC designator for Matte Tin (Sn)
- * This package is Pb-free. The Pb-free JEDEC designator (3e) can be found on the outer packaging for this package.

Note: In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line, thus limiting the number of available characters for customer-specific information.
MCP9700/9700A and MCP9701/9701A

5-Lead Plastic Small Outline Transistor (LT) (SC-70)

<table>
<thead>
<tr>
<th>Units</th>
<th>INCHES</th>
<th>MILLIMETERS*</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dimension Limits</td>
<td>MIN</td>
<td>NOM</td>
</tr>
<tr>
<td>Number of Pins</td>
<td>n</td>
<td>5</td>
</tr>
<tr>
<td>Pitch</td>
<td>P</td>
<td>.026 (BSC)</td>
</tr>
<tr>
<td>Overall Height</td>
<td>A</td>
<td>.031</td>
</tr>
<tr>
<td>Molded Package Thickness</td>
<td>A2</td>
<td>.031</td>
</tr>
<tr>
<td>Standoff</td>
<td>A1</td>
<td>.000</td>
</tr>
<tr>
<td>Overall Width</td>
<td>E</td>
<td>.071</td>
</tr>
<tr>
<td>Molded Package Width</td>
<td>E1</td>
<td>.045</td>
</tr>
<tr>
<td>Overall Length</td>
<td>D</td>
<td>.071</td>
</tr>
<tr>
<td>Foot Length</td>
<td>L</td>
<td>.004</td>
</tr>
<tr>
<td>Top of Molded Pkg to Lead Shoulder</td>
<td>Q1</td>
<td>.004</td>
</tr>
<tr>
<td>Lead Thickness</td>
<td>c</td>
<td>.004</td>
</tr>
<tr>
<td>Lead Width</td>
<td>B</td>
<td>.006</td>
</tr>
</tbody>
</table>

* Controlling Parameter

Notes:

Dimensions D and E1 do not include mold flash or protrusions. Mold flash or protrusions shall not exceed .005" (0.127mm) per side.

BSC: Basic Dimension. Theoretically exact value shown without tolerances.

See ASME Y14.5M

JEITA (EIAJ) Standard: SC-70

Drawing No. C04-061

Revised 07-19-05
### 3-Lead Plastic Transistor Outline (TO) (TO-92)

**Dimensions**

- **Mold Draft Angle Top**: α
- **Mold Draft Angle Bottom**: β
- **Pitch (p)**: .050
- **Bottom to Package Flat (A)**: 1.30
- **Overall Width (E1)**: 1.75
- **Overall Length (D)**: 1.70
- **Molded Package Radius (R)**: .085
- **Tip to Seating Plane (L)**: .500
- **Lead Thickness (c)**: .014
- **Lead Width (B)**: .016
- **Mold Draft Angle Top**: 4
- **Mold Draft Angle Bottom**: 2

<table>
<thead>
<tr>
<th>Units</th>
<th>INCHES*</th>
<th>MILLIMETERS</th>
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</thead>
<tbody>
<tr>
<td>Number of Pins</td>
<td>n</td>
<td>3</td>
</tr>
<tr>
<td>Pitch</td>
<td>p</td>
<td>.050</td>
</tr>
<tr>
<td>Bottom to Package Flat</td>
<td>A</td>
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</tr>
<tr>
<td>Overall Width</td>
<td>E1</td>
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<tr>
<td>Overall Length</td>
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<tr>
<td>Molded Package Radius</td>
<td>R</td>
<td>.085</td>
</tr>
<tr>
<td>Tip to Seating Plane</td>
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<td>.500</td>
</tr>
<tr>
<td>Lead Thickness</td>
<td>c</td>
<td>.014</td>
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<tr>
<td>Lead Width</td>
<td>B</td>
<td>.016</td>
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<tr>
<td>Mold Draft Angle Top</td>
<td>α</td>
<td>4</td>
</tr>
<tr>
<td>Mold Draft Angle Bottom</td>
<td>β</td>
<td>2</td>
</tr>
</tbody>
</table>

* Controlling Parameter

**Notes:**

- Dimensions D and E1 do not include mold flash or protrusions. Mold flash or protrusions shall not exceed .010” (0.254mm) per side.
- JEDEC Equivalent: TO-92
- Drawing No. C04-101
APPENDIX A: REVISION HISTORY

Revision C (June 2006)

• Added the MCP9700A and MCP9701A devices to data sheet
• Added TO92 package for the MCP9700/MCP9701

Revision B (October 2005)

The following is the list of modifications:
• Added Section 3.0 “Pin Descriptions”
• Added the Linear Active Thermistor™ IC trademark
• Removed the 2nd order temperature equation and the temperature coefficient histogram
• Added a reference to AN1001 and corresponding verbiage
• Added Figure 4-2 and corresponding verbiage

Revision A (November 2005)

• Original Release of this Document.
## PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, refer to the factory or the listed sales office.

### PART NO. - X - /XX

<table>
<thead>
<tr>
<th>Device</th>
<th>Temperature Range</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>MCP9700T</td>
<td>E = -40°C to +125°C</td>
<td>LT = Plastic Small Outline Transistor, 5-lead</td>
</tr>
<tr>
<td>MCP9700AT</td>
<td></td>
<td>TO = Plastic Plastic Transistor Outline, 3-lead (MCP9700, MCP9701 only)</td>
</tr>
<tr>
<td>MCP9701T</td>
<td></td>
<td>LT = Plastic Small Outline Transistor, 5-lead</td>
</tr>
<tr>
<td>MCP9701AT</td>
<td></td>
<td>TO = Plastic Plastic Transistor Outline, 3-lead (MCP9700, MCP9701 only)</td>
</tr>
</tbody>
</table>

### Examples:

- MCP9700T-E/LT: Linear Active Thermistor™ IC, Tape and Reel, 5LD SC-70 package.
- MCP9700-E/TO: Linear Active Thermistor™ IC, 3LD TO-92 package.
- MCP9700AT-E/LT: Linear Active Thermistor™ IC, Tape and Reel, 5LD SC-70 package.
- MCP9701T-E/LT: Linear Active Thermistor™ IC, Tape and Reel, 5LD SC-70 package.
- MCP9701-E/TO: Linear Active Thermistor™ IC, 3LD TO-92 package.
- MCP9701AT-E/LT: Linear Active Thermistor™ IC, Tape and Reel, 5LD SC-70 package.
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