Features

- Fan Wear-Out Detection for 2-Wire Linear-Controlled Fans
- Replacement System for 3-Wire Fans
- Fan Alert Signal when Fan Speed is below Programmed Threshold
- CLEAR Capability for Eliminating False Alarm
- Low Operating Current, 90 µA (typ.)
- VDD Range 3.0V to 5.5V
- Available in a 6-Pin SOT-23 Package

Applications

- Protection for Linear-Controlled Fans
- Power Supplies
- Industrial Equipment
- PCs and Notebooks
- Data Storage
- Data Communications Equipment
- Instrumentation

General Description

The TC670 is an integrated fan speed sensor that predicts and/or detects fan failure, preventing thermal damage to systems with cooling fans. When the fan speed falls below a user-specified level, the TC670 asserts an ALERT signal. With this design, a critical minimum fan speed is determined by the user. The fan alert level is then set with a resistor divider on the THRESHOLD pin (Pin 1) of the TC670. When the minimum fan speed is reached, the ALERT pin (Pin 5) changes from a digital high to low. This failure detection works with all linear-controlled 2-wire fans. The TC670 eliminates the need for 3-wire fan solutions.

A CLEAR option can be used to reset the ALERT signal, allowing the flexibility of connecting the ALERT output of the TC670 with other ALERT/FAULT interrupts in the system. This feature can be implemented so that false fan fault conditions do not initiate system shutdown.

The TC670 is specified to operate over the full industrial temperature range of -40°C to +85°C. The TC670 is offered in a 6-pin SOT-23 pin package and consumes 90 µA (typ.) during operation. The space-saving package and low power consumption make this device an ideal choice for systems requiring fan speed monitoring.

Typical Application Circuit
1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings†

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>( V_{DD} )</td>
<td>3.0</td>
<td>5.5</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>All Inputs and Outputs</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Output Short-Circuit Current</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Junction Temperature, ( T_J )</td>
<td></td>
<td></td>
<td></td>
<td>150°C</td>
</tr>
<tr>
<td>ESD protection on all pins</td>
<td></td>
<td></td>
<td></td>
<td>( \geq 4 \text{ kV} )</td>
</tr>
</tbody>
</table>

Operating Temperature Range..............-40°C to +85°C
Storage Temperature Range................-55°C to +150°C

† Notice: Stresses above those listed under "Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operation listings of this specification is not implied. Exposure to maximum rating conditions for extended periods may affect device reliability.

DC CHARACTERISTICS

Electrical Specifications: Unless otherwise specified, all limits are specified at +25°C, \( V_{DD} = 3.0 \text{V to 5.5V} \), CLEAR = Low. Boldface type specifications apply for temperature range of -40°C to +85°C.

<table>
<thead>
<tr>
<th>Parameters</th>
<th>Sym</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power Supply</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Supply Voltage ( V_{DD} )</td>
<td>3.0</td>
<td>5.5</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Supply Current ( I_{DD} )</td>
<td></td>
<td>90</td>
<td>150</td>
<td>µA</td>
<td></td>
</tr>
<tr>
<td>CLEAR Input</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Logic Input High Level</td>
<td>( V_{IH} )</td>
<td>0.8</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Logic Input Low Level</td>
<td>( V_{IL} )</td>
<td></td>
<td></td>
<td>0.2</td>
<td>V</td>
</tr>
<tr>
<td>SENSE Input</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input Level Threshold Voltage ( V_{TH(SENSE)} )</td>
<td></td>
<td>124</td>
<td></td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td>Input Resistance ( R_{SENSE} )</td>
<td></td>
<td>50</td>
<td></td>
<td></td>
<td>kΩ</td>
</tr>
<tr>
<td>THRESHOLD Input</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input Voltage Minimum</td>
<td></td>
<td>0.0</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Input Voltage Maximum</td>
<td></td>
<td>2.4</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Input Resistance</td>
<td></td>
<td>100</td>
<td></td>
<td></td>
<td>MΩ</td>
</tr>
<tr>
<td>Programmed Fan Speed Alert Accuracy (Note 1)</td>
<td>( \text{ALERT}^\text{ACC} )</td>
<td>-10</td>
<td></td>
<td>+10</td>
<td>%</td>
</tr>
<tr>
<td>ALERT Output</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Output Low Voltage</td>
<td>( V_{LOW} )</td>
<td></td>
<td></td>
<td>0.3</td>
<td>V</td>
</tr>
<tr>
<td>Output Delay Time</td>
<td>( t_{DELY} )</td>
<td></td>
<td></td>
<td>176</td>
<td>ms</td>
</tr>
</tbody>
</table>

Temperature Ranges

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Specified Temperature Range</td>
<td>-40</td>
<td>+85</td>
<td></td>
<td>°C</td>
</tr>
<tr>
<td>Operating Temperature Range</td>
<td>-40</td>
<td>+125</td>
<td></td>
<td>°C</td>
</tr>
</tbody>
</table>

Thermal Package Resistances

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Thermal Resistance, 6L-SOT-23</td>
<td></td>
<td></td>
<td></td>
<td>°C/W</td>
</tr>
</tbody>
</table>

Note 1: The TC670 will operate properly over the entire power supply range of 3.0V to 5.5V. As \( V_{DD} \) varies from 3.0V, accuracy will degrade based on the percentage of \( V_{DD} \), as shown in Section 2.0, “Typical Performance Curves”.

TABLE 1-1: PIN FUNCTION TABLE

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>THRESHOLD</td>
<td>Analog Input</td>
</tr>
<tr>
<td>GND</td>
<td>Ground Terminal</td>
</tr>
<tr>
<td>CLEAR</td>
<td>Digital Input</td>
</tr>
<tr>
<td>VDD</td>
<td>Bias Supply Input</td>
</tr>
<tr>
<td>ALERT</td>
<td>Digital (Open-Drain) Output</td>
</tr>
<tr>
<td>SENSE</td>
<td>Analog Input</td>
</tr>
</tbody>
</table>
2.0 TYPICAL PERFORMANCE CURVES

Note: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.

Note: Unless otherwise indicated, all limits are specified at +25°C, VDD = 3.0V to 5.5V, CLEAR = Low.

**FIGURE 2-1:** Supply Current vs. Supply Voltage.

**FIGURE 2-2:** Fan Speed vs. Threshold Voltage.

**FIGURE 2-3:** Fan Speed vs. Threshold Voltage.

**FIGURE 2-4:** ALERT VLOW vs. ALERT ISINK.

**FIGURE 2-5:** Fan Speed vs. ALERTACC.

**FIGURE 2-6:** ALERT Output Delay vs. Power Supply Voltage.
Note: Unless otherwise indicated, all limits are specified at +25°C, $V_{DD} = 3.0$V to 5.5V, CLEAR = Low.

FIGURE 2-7: CLEAR pin high to ALERT pin high Timing Diagram.
3.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in Table 3-1.

**TABLE 3-1: PIN FUNCTION TABLE**

<table>
<thead>
<tr>
<th>Pin No.</th>
<th>Symbol</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>THRESHOLD</td>
<td>Analog Input</td>
</tr>
<tr>
<td>2</td>
<td>GND</td>
<td>Ground Terminal</td>
</tr>
<tr>
<td>3</td>
<td>CLEAR</td>
<td>Digital Input</td>
</tr>
<tr>
<td>4</td>
<td>VDD</td>
<td>Bias Supply Input</td>
</tr>
<tr>
<td>5</td>
<td>ALERT</td>
<td>Digital (Open-Drain) Output</td>
</tr>
<tr>
<td>6</td>
<td>SENSE</td>
<td>Analog Input</td>
</tr>
</tbody>
</table>

3.1 Analog Input (THRESHOLD)

The voltage set at the THRESHOLD input represents the fan speed at which the TC670 will signal a fan speed warning by pulling the ALERT output low. The threshold voltage to fan speed correlation can be seen in Figures 2-2 and 2-3.

3.2 Ground (GND)

The GND pin (Pin 2) of the TC670 should be connected directly to the analog ground plane of the circuit board. Care should be taken to keep this pin away from switching signals, such as the fan excitation signals in order to avoid false signals on the SENSE pin.

3.3 Digital Input (CLEAR)

The CLEAR input is used to reset or blank the ALERT output. When the CLEAR input is driven high, the ALERT output will be high-impedance (the ALERT output requires a pull-up resistor).

3.4 Bias Supply Input (VDD)

Bias Supply Input, 3.0V to 5.5V. The bias supply input should be bypassed to ground with a 0.1 µF ceramic capacitor.

3.5 Digital (Open-Drain) Output (ALERT)

The ALERT output is an open-drain output that requires an external pull-up resistor. The ALERT output is pulled low when the sensed fan speed (detected by the pulses occurring at the SENSE input) falls below the speed that is represented by the voltage at the THRESHOLD pin. The ALERT output is latched in this state until power is cycled or the CLEAR input is toggled.

3.6 Analog Input (SENSE)

Voltage pulses, which are generated by the fan current flowing through a sense resistor, are detected at the SENSE pin and used to calculate the fan speed.
4.0 DETAILED DESCRIPTION

The TC670 is an integrated fan speed sensor that predicts/detects fan failure, consequently preventing thermal damage to systems with cooling fans. When the fan speed falls below a user-programmed threshold level, the TC670 asserts an ALERT signal. This threshold is set with an external resistor divider network.

![TC670 Block Diagram](image)

**FIGURE 4-1:** TC670 Block Diagram.

As shown in Figure 4-1, the TC670 senses the fan pulses and internally converts those pulses from a frequency into an analog voltage. This voltage is then compared with the DC voltage at the THRESHOLD pin. If the converted frequency-to-voltage value from the fan's pulses falls below the threshold voltage, the ALERT output is pulled low.

In a 3.0V system, the external fan alert level on the THRESHOLD pin can be designed from 0.0V (stalled fan) to 2.4V (for 13,000 RPM) to cover most of the common fan speeds. This failure detection system works with linear-controlled 2-wire fans and eliminates the need for 3-wire fans. The TC670 can also work with 3-wire fans either by using the SENSE circuit or by directly sensing the RPM output from the 3rd wire.

A CLEAR pin is provided to allow the user to reset the ALERT pin status back to a high state. This clear option also allows the flexibility of connecting the ALERT output of the TC670 with other alert/fault interrupts in the system without having a risk of a system shutdown due to false fan fault condition.

**4.1 SENSE Input**

As shown in Figure 4-2, the SENSE input (Pin 6) is connected to the sense resistor \( R_{SENSE} \) through a capacitor \( C_{SENSE} \). The low value current sensing resistor \( R_{SENSE} \) is connected between the ground return leg of the fan and the fan bias ground. During normal fan operation, commutation occurs as each pole of the fan is energized. This causes the fan current to be an AC waveform with fast falling edges.

These short, rapid changes in fan current cause a corresponding dV/dt voltage across the sense resistor, as well as a corresponding dI/dt current through the sense capacitor. The current through \( C_{SENSE} \) is terminated with the internal 50 kΩ input resistance at the SENSE pin of the TC670. When positive-going fan pulses at the SENSE input are greater than 124 mV (typ.), the TC670 latches-in those voltage spikes. This 124 mV (typ.) SENSE input built-in threshold reduces false triggering errors caused by extraneous noise pulses associated with a running fan. The presence and frequency of these pulses is a direct indication of fan operation and fan speed.

![Typical Application Circuit](image)

**FIGURE 4-2:** Typical Application Circuit.
The design of the proper input SENSE circuitry is a matter of scaling Rsense to provide the necessary amount of gain and proper selection of the sensing capacitor. The following table (Table 4-1) lists some recommended values for Rsense according to the nominal operating current of the fan. Please note that the current draw specified by the fan manufacturer may be a worst-case rating and not the fan's nominal operating current. If the fan current falls between two of the values listed, it is recommended that the higher value resistor is used.

TABLE 4-1: RECOMMENDED VALUES FOR Rsense PER FIGURE 4-2

<table>
<thead>
<tr>
<th>Nominal Fan Current (mA)</th>
<th>Rsense (Ω)</th>
</tr>
</thead>
<tbody>
<tr>
<td>100</td>
<td>4.7</td>
</tr>
<tr>
<td>200</td>
<td>2.4</td>
</tr>
<tr>
<td>300</td>
<td>1.8</td>
</tr>
<tr>
<td>400</td>
<td>1.3</td>
</tr>
<tr>
<td>500</td>
<td>1.0</td>
</tr>
<tr>
<td>600</td>
<td>0.8</td>
</tr>
</tbody>
</table>

A 0.1 µF ceramic capacitor is recommended for Csense. Smaller capacitor values will require larger sense resistors, whereas larger capacitors are more expensive and occupy more board space.

4.2 THRESHOLD Input

The voltage at the THRESHOLD input sets the equivalent minimum allowable fan speed for the application. As shown in Section 2.0, “Typical Performance Curves”, the relationship between the threshold voltage and minimum fan speed is also power supply and temperature dependant.

All the values for the threshold voltage that are shown in these graphs represent typical numbers and might not be optimized for all fans in all applications. To ensure accurate fan speed monitoring of a specific fan in a specific application, the user must perform a one-time correlation check with the prototype.

There are two techniques that can be used to calibrate the system. One approach is to find the fan’s full-scale capability and mathematically estimate the minimum acceptable speed of the fan. A second technique is to identify the fan’s minimum speed and calibrate the threshold voltage accordingly.

4.2.1 THRESHOLD CALIBRATION USING FAN’S FULL SCALE SPEED

The fan should first be run at full speed. At full speed, the threshold voltage level should be adjusted until the ALERT output is asserted. With this full-scale value of the threshold voltage, the value can be scaled down to the fan fault speed as a percentage of the full speed. For example, if the fan full speed threshold voltage is 1.5V, then the fan fault threshold voltage at 30% of full speed would be 30% x 1.5V = 0.45V.

4.2.2 THRESHOLD CALIBRATION USING FAN’S MINIMUM ALLOWABLE SPEED ESTIMATE

For a more exact fan fault trip point, the user can run the fan at its minimum allowed speed. At this speed, the threshold voltage can be adjusted until the ALERT output is asserted.

4.3 CLEAR Input

The CLEAR input allows the user to reset the ALERT pin to a high status. This is an active-high input. Consequently, as long as CLEAR is high, ALERT will always be high as well. To allow ALERT to operate correctly, CLEAR must be held low. This feature can be implemented so that false fan fault conditions do not initiate system shutdown.

4.4 ALERT Output

The ALERT output is an open-drain output capable of sinking 2.5 mA (typ). The ALERT output is asserted whenever the detected fan speed equals or falls below the equivalent voltage set at the threshold pin. The ALERT output is only deactivated once the CLEAR pin is brought to a high state. Although the absolute maximum sink current of this pin is 25 mA, it is recommended that the current sinking into the ALERT output does not exceed 20 mA.

4.5 Power Supply Input (VDD)

To assure proper operation of the TC670 in a noisy environment where the fans are running, the VDD pin (Pin 4) must be decoupled with a 0.1 µF capacitor, as shown in Figure 4-1. This capacitor should be located as close to the TC670 VDD pin as possible, as well as being promptly terminated to the ground plane. A ceramic capacitor is recommended.

4.6 Ground Terminal (GND)

The GND pin (Pin 2) of the TC670 should be connected directly to the analog ground plane of the circuit board. Care should be taken to keep this pin away from switching signals, such as the fan excitation signals in order to avoid false signals on the SENSE pin.
5.0 PACKAGE INFORMATION

5.1 Package Marking Information

6-Pin SOT-23A (EIAJ SC-74) Device

1 & 2 = part number code
3 = year and quarter code
4 = lot ID number

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>TC670ECH</td>
<td>DA</td>
</tr>
</tbody>
</table>

5.2 Taping Form

Component Taping Orientation for 6-Pin SOT-23A (EIAJ SC-74) Devices

Carrier Tape, Number of Components Per Reel and Reel Size:

<table>
<thead>
<tr>
<th>Package</th>
<th>Carrier Width (W)</th>
<th>Pitch (P)</th>
<th>Part Per Full Reel</th>
<th>Reel Size</th>
</tr>
</thead>
<tbody>
<tr>
<td>6-Pin SOT-23A</td>
<td>8 mm</td>
<td>4 mm</td>
<td>3000</td>
<td>7 in.</td>
</tr>
</tbody>
</table>
5.3  Package Dimensions (6-Pin SOT-23)

Note:  For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging

<table>
<thead>
<tr>
<th>Units</th>
<th>INCHES*</th>
<th>MILLIMETERS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dimension Limits</td>
<td>MIN</td>
<td>NOM</td>
</tr>
<tr>
<td>Number of Pins</td>
<td>n</td>
<td>6</td>
</tr>
<tr>
<td>Pitch</td>
<td>P</td>
<td>.038</td>
</tr>
<tr>
<td>Outside lead pitch (basic)</td>
<td>p1</td>
<td>.075</td>
</tr>
<tr>
<td>Overall Height</td>
<td>A</td>
<td>.035</td>
</tr>
<tr>
<td>Molded Package Thickness</td>
<td>A2</td>
<td>.035</td>
</tr>
<tr>
<td>Standoff</td>
<td>A1</td>
<td>.000</td>
</tr>
<tr>
<td>Overall Width</td>
<td>E</td>
<td>.102</td>
</tr>
<tr>
<td>Molded Package Width</td>
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<td>.059</td>
</tr>
<tr>
<td>Overall Length</td>
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<td>.110</td>
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<tr>
<td>Foot Length</td>
<td>L</td>
<td>.014</td>
</tr>
<tr>
<td>Foot Angle</td>
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<td>5</td>
</tr>
<tr>
<td>Lead Thickness</td>
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<td>.004</td>
</tr>
<tr>
<td>Lead Width</td>
<td>B</td>
<td>.014</td>
</tr>
<tr>
<td>Mold Draft Angle Top</td>
<td>0</td>
<td>5</td>
</tr>
<tr>
<td>Mold Draft Angle Bottom</td>
<td>0</td>
<td>5</td>
</tr>
</tbody>
</table>

*Controlling Parameter

Notes:
Dimensions D and E1 do not include mold flash or protrusions. Mold flash or protrusions shall not exceed .005" (0.127mm) per side.

JEITA (formerly EIAJ) equivalent: SC-74A
Drawing No. C04-120
6.0 REVISION HISTORY

Revision D (December 2012)

Added a note to each package outline drawing.
PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, refer to the factory or the listed sales office.

<table>
<thead>
<tr>
<th>PART NO.</th>
<th>X</th>
<th>XXXX</th>
</tr>
</thead>
<tbody>
<tr>
<td>Device</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Temperature Range</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Package</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Examples:
a) TC670ECHTR: Predictive Fan Failure Detector, SOT-23 package.

Sales and Support

Data Sheets
Products supported by a preliminary Data Sheet may have an errata sheet describing minor operational differences and recommended workarounds. To determine if an errata sheet exists for a particular device, please contact one of the following:

1. Your local Microchip sales office
2. The Microchip Worldwide Site (www.microchip.com)

Please specify which device, revision of silicon and Data Sheet (include Literature #) you are using.

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