**TC962**

**High Current Charge Pump DC-to-DC Converter**

**Features:**
- Pin Compatible With TC7662/ICL7662/SI7661
- High Output Current 80 mA
- No External Diodes Required
- Wide Operating Range 3V to 18V
- Low Output Impedance 28Ω Typ.
- No Low Voltage Terminal Required
- Application Zener On-Chip
- OSC Frequency Doubling Pin Option for Smaller Output Capacitors

**Applications:**
- Laptop Computers
- Disk Drives
- Process Instrumentation
- μP-Based Controllers

**Device Selection Table**

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
<th>Operating Temp. Range</th>
</tr>
</thead>
<tbody>
<tr>
<td>TC962COE</td>
<td>16-Pin SOIC Wide</td>
<td>0°C to +70°C</td>
</tr>
<tr>
<td>TC962CPA</td>
<td>8-Pin Plastic DIP</td>
<td>0°C to +70°C</td>
</tr>
<tr>
<td>TC962EPA</td>
<td>8-Pin Plastic DIP</td>
<td>-40°C to +85°C</td>
</tr>
<tr>
<td>TC962IJA</td>
<td>8-Pin CERDIP</td>
<td>-25°C to +85°C</td>
</tr>
<tr>
<td>TC962MJA</td>
<td>8-Pin CERDIP</td>
<td>-55°C to +125°C</td>
</tr>
</tbody>
</table>

**General Description:**

The TC962 is an advanced version of the industry standard TC7662 high voltage DC-to-DC converter. Using improved design techniques and CMOS construction, the TC962 can source as much as 80 mA versus the 7662’s 20 mA capability.

As an inverter, the TC962 can put out voltages as high as 18V and as low as 3V without the need for external diodes. The output impedance of the device is a low 28Ω (with the proper capacitors), voltage conversion efficiency is 99.9%, and power conversion efficiency is 97%.

The low voltage terminal (pin 6) required in some TC7662 applications has been eliminated. Grounding this terminal will double the oscillator frequency from 12 kHz to 24 kHz. This will allow the use of smaller capacitors for the same output current and ripple, in most applications. Only two external capacitors are required for inverter applications. In the event an external clock is needed to drive the TC962 (such as paralleling), driving this pin directly will cause the internal oscillator to sync to the external clock.

Pin 1, which is used as a test pin on the 7662, is a voltage reference Zener on the TC962. This Zener (6.4V at 5 mA) has a dynamic impedance of 12Ω and is intended for use where the TC962 is supplying current to external regulator circuitry and a reference is needed for the regulator circuit. (See Section 3.0 “Applications Information” Applications Information).

The TC962 is compatible with the LTC1044, SI7661 and ICL7662. It should be used in designs that require greater power and/or less input to output voltage drop. It offers superior performance over the ICL7660S.

**Package Type**

<table>
<thead>
<tr>
<th>16-Pin SOIC Wide</th>
<th>8-Pin DIP</th>
<th>Zener Cathode</th>
<th>8-Pin CERDIP</th>
</tr>
</thead>
<tbody>
<tr>
<td>16</td>
<td>NC</td>
<td>16</td>
<td>NC</td>
</tr>
<tr>
<td>15</td>
<td>NC</td>
<td>15</td>
<td>NC</td>
</tr>
<tr>
<td>14</td>
<td>VDD</td>
<td>14</td>
<td>COsc</td>
</tr>
<tr>
<td>13</td>
<td>NC</td>
<td>13</td>
<td>NC</td>
</tr>
<tr>
<td>12</td>
<td>FREQ x 2</td>
<td>12</td>
<td>NC</td>
</tr>
<tr>
<td>11</td>
<td>NC</td>
<td>11</td>
<td>NC</td>
</tr>
<tr>
<td>10</td>
<td>VOUT</td>
<td>10</td>
<td>VOUT</td>
</tr>
<tr>
<td>9</td>
<td>NC</td>
<td>9</td>
<td>NC</td>
</tr>
<tr>
<td>8</td>
<td>NC</td>
<td>8</td>
<td>NC</td>
</tr>
<tr>
<td>7</td>
<td>VOUT</td>
<td>7</td>
<td>VOUT</td>
</tr>
<tr>
<td>6</td>
<td>FREQ x 2</td>
<td>6</td>
<td>NC</td>
</tr>
<tr>
<td>5</td>
<td>NC</td>
<td>5</td>
<td>NC</td>
</tr>
<tr>
<td>4</td>
<td>VOUT</td>
<td>4</td>
<td>NC</td>
</tr>
<tr>
<td>3</td>
<td>COsc</td>
<td>3</td>
<td>NC</td>
</tr>
<tr>
<td>2</td>
<td>NC</td>
<td>2</td>
<td>NC</td>
</tr>
<tr>
<td>1</td>
<td>VDD</td>
<td>1</td>
<td>NC</td>
</tr>
</tbody>
</table>
Functional Block Diagram

TC962

- Comparator with Hysteresis
- Level Shifters
- Zener Cathode: 6.4V
- OSC/C\(^{-}\) Timing
- FREQ x 2
- VREF
- P SW1
- N SW4
- CAP\(^{+}\)
- CP
- External
- N SW2
- N SW3
- CAP\(^{-}\)
- OUT
- CR\(_{\text{EXT}}\)
- RL
- VOUT
- V\(_{DD}\)
- GND

© 2006 Microchip Technology Inc.
1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings*

Supply Voltage (VDD to GND) .................................... +18V
Input Voltage Any Pin ........................................ (VDD +0.3) to (VSS -0.3) (Note 1)
Current Into Any Pin ........................................... 10 mA
ESD Protection ................................................ ±2000V
Output Short Circuit ........... Continuous (at 5.5V Input)
Package Power Dissipation (TA ≤ 70°C)
  SOIC ....................................................... 760 mW
  PDIP ........................................................ 730 mW
  CERDIP .................................................. 800 mW
Package Thermal Resistance
  CERDIP, RθJ-A .........................................90°C/W
  PDIP, RθJ-A ............................................ 140°C/W
Operating Temperature Range
  CPA, COE ....................................... 0°C to +70°C
  IJA................................................ -25°C to +85°C
  EPA .............................................. -40°C to +85°C
  MJA............................................ -55°C to +125°C
Storage Temperature Range ............. -65°C to +150°C

*Stresses above those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operation sections of the specifications is not implied. Exposure to Absolute Maximum Rating conditions for extended periods may affect device reliability.

TC962 ELECTRICAL SPECIFICATIONS

Electrical Characteristics: VDD = 15V, TA = 25°C (See Figure ) unless otherwise noted.

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
<th>Test Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>VDD</td>
<td>Supply Voltage</td>
<td>3</td>
<td>—</td>
<td>18</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>IS</td>
<td>Supply Current</td>
<td></td>
<td></td>
<td></td>
<td>µA</td>
<td>R_L = ∞</td>
</tr>
<tr>
<td>VDD = 15V</td>
<td>— 510 700</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>TA = +25°C</td>
</tr>
<tr>
<td></td>
<td>VDD = 5V</td>
<td>—</td>
<td>560</td>
<td>—</td>
<td></td>
<td>0 ≤ TA ≤ +70°C</td>
</tr>
<tr>
<td></td>
<td></td>
<td>—</td>
<td>650</td>
<td>—</td>
<td></td>
<td>-55°C ≤ TA ≤ +125°C</td>
</tr>
<tr>
<td></td>
<td></td>
<td>—</td>
<td>190</td>
<td>—</td>
<td></td>
<td>TA = +25°C</td>
</tr>
<tr>
<td></td>
<td></td>
<td>—</td>
<td>210</td>
<td>—</td>
<td></td>
<td>0 ≤ TA ≤ +70°C</td>
</tr>
<tr>
<td></td>
<td></td>
<td>—</td>
<td>210</td>
<td>—</td>
<td></td>
<td>-55°C ≤ TA ≤ +125°C</td>
</tr>
<tr>
<td>R_O</td>
<td>Output Source Resistance</td>
<td>—</td>
<td>32</td>
<td>37</td>
<td>Ω</td>
<td>I_L = 20 mA, V_DD = 15V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>—</td>
<td>35</td>
<td>40</td>
<td></td>
<td>I_L = 80 mA, V_DD = 15V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>—</td>
<td>50</td>
<td></td>
<td></td>
<td>I_L = 3 mA, V_DD = 5V</td>
</tr>
<tr>
<td>F_OSC</td>
<td>Oscillator Frequency</td>
<td>—</td>
<td>12</td>
<td>—</td>
<td>kHz</td>
<td>Pin 6 Open</td>
</tr>
<tr>
<td></td>
<td></td>
<td>—</td>
<td>24</td>
<td>—</td>
<td></td>
<td>Pin 6 GND</td>
</tr>
<tr>
<td>P_EFF</td>
<td>Power Efficiency</td>
<td>93</td>
<td>97</td>
<td>—</td>
<td>%</td>
<td>R_L = 2 kΩ</td>
</tr>
<tr>
<td>V_DEF</td>
<td>Voltage Efficiency</td>
<td>99</td>
<td>99.9</td>
<td>—</td>
<td>%</td>
<td>R_L = ∞</td>
</tr>
<tr>
<td></td>
<td></td>
<td>96</td>
<td></td>
<td></td>
<td></td>
<td>Over temperature range</td>
</tr>
<tr>
<td>V_Z</td>
<td>Zener Voltage</td>
<td>6.0</td>
<td>6.2</td>
<td>6.4</td>
<td>V</td>
<td>I_Z = 5 mA</td>
</tr>
<tr>
<td>Z_ZT</td>
<td>Zener Impedance</td>
<td>—</td>
<td>12</td>
<td>—</td>
<td>Ω</td>
<td>I_L = 2.5 mA to 7.5 mA</td>
</tr>
</tbody>
</table>

Note 1: Connecting any input terminal to voltages greater than V” or less than GND may cause destructive latch-up. It is recommended that no inputs from sources operating from external supplies be applied prior to “power-up” of the TC962.
2.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in Table 2-1.

<table>
<thead>
<tr>
<th>Pin No. (8-Pin DIP) Symbol</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 Zener Cathode</td>
<td>Cathode of internal Zener diode.</td>
</tr>
<tr>
<td>2 C^+</td>
<td>Positive side of external CP capacitor (pump cap).</td>
</tr>
<tr>
<td>3 GND</td>
<td>Ground terminal.</td>
</tr>
<tr>
<td>4 C^-</td>
<td>Negative side of external CP capacitor (pump cap).</td>
</tr>
<tr>
<td>5 V_OUT</td>
<td>Output voltage.</td>
</tr>
<tr>
<td>6 FREQ x 2</td>
<td>If grounded, frequency doubles.</td>
</tr>
<tr>
<td>7 C_{OSC}</td>
<td>Capacitor to GND will decrease frequency.</td>
</tr>
<tr>
<td>8 V_{DD}</td>
<td>Input voltage.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Pin No. (16-Pin SOIC) Symbol</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 Zener Cathode</td>
<td>Cathode of internal Zener diode.</td>
</tr>
<tr>
<td>2 NC</td>
<td>No connect.</td>
</tr>
<tr>
<td>3 C^+</td>
<td>Positive side of external CP capacitor (pump cap).</td>
</tr>
<tr>
<td>4 NC</td>
<td>No connect.</td>
</tr>
<tr>
<td>5 GND</td>
<td>Ground terminal.</td>
</tr>
<tr>
<td>6 NC</td>
<td>No connect.</td>
</tr>
<tr>
<td>7 C^-</td>
<td>Negative side of external CP capacitor (pump cap).</td>
</tr>
<tr>
<td>8 NC</td>
<td>No connect.</td>
</tr>
<tr>
<td>9 NC</td>
<td>No connect.</td>
</tr>
<tr>
<td>10 V_OUT</td>
<td>Output voltage.</td>
</tr>
<tr>
<td>11 NC</td>
<td>No connect.</td>
</tr>
<tr>
<td>12 FREQ x 2</td>
<td>If grounded, frequency doubles.</td>
</tr>
<tr>
<td>13 NC</td>
<td>No connect.</td>
</tr>
<tr>
<td>14 C_{OSC}</td>
<td>Capacitor to GND will decrease frequency.</td>
</tr>
<tr>
<td>15 NC</td>
<td>No connect.</td>
</tr>
<tr>
<td>16 V_{DD}</td>
<td>Input voltage.</td>
</tr>
</tbody>
</table>
3.0 APPLICATIONS INFORMATION

3.1 Theory of Operation

The TC962 is a capacitive pump (sometimes called a switched capacitor circuit), where four MOSFET switches control the charge and discharge of a capacitor.

The functional block diagram shows how the switching action works. SW1 and SW2 are turned on simultaneously, charging CP to the supply voltage, V IN. This assumes that the on resistance of the MOSFETs in series with the capacitor results in a charging time (3 time constants) that is less than the on time provided by the oscillator frequency as shown:

\[3 \left( R_{DS(ON)} \right) C_P < \frac{C_P}{0.5 \cdot f_{OSC}}\]

In the next cycle, SW1 and SW2 are turned off and after a very short interval of all switches being off (this prevents large currents from occurring due to cross conduction), SW3 and SW4 are turned on. The charge in CP is then transferred to CR, but with the polarity inverted. In this way, a negative voltage is now derived.

An oscillator supplies pulses to a flip-flop that is then fed to a set of level shifters. These level shifters then drive each set of switches at one-half the oscillator frequency.

The oscillator has two pins that control the frequency of oscillation. Pin 7 can have a capacitor added that is returned to ground. This will lower the frequency of the oscillator by adding capacitance to the timing capacitor internal to the TC962. Grounding pin 6 will turn on a current source and double the frequency. This will double the charge current going into the internal capacitor, as well as any capacitor added to pin 7.

A Zener diode has been added to the TC962 for use as a reference in building external regulators. This Zener runs from pin 1 to ground.

3.2 Latch-Up

All CMOS structures contain a parasitic SCR. Care must be taken to prevent any input from going above or below the supply rail, or latch-up will occur. The result of latch-up is an effective short between V DD and V SS. Unless the power supply input has a current limit, this latch-up phenomena will result in damage to the device. (See AN763, Latch-up Protection for MOSFET Drivers.)
4.0 TYPICAL CHARACTERISTICS

Note: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.

Circuit of Figure, CP = CR = 10 μF, CPESR ≈ CRESR = 1 Ω.

Supply Current vs. Temperature

Oscillator Frequency vs. C_DSC

Frequency vs. Temperature

Output Resistance vs. Temperature

Current vs. Zener Voltage

Power Conversion Efficiency vs. I_LOAD

Output Resistance vs. Input Voltage
5.0 PACKAGING INFORMATION

5.1 Package Marking Information

Package marking data not available at this time.

5.2 Taping Form

Component Taping Orientation for 16-Pin SOIC (Wide) Devices

Carrier Tape, Number of Components Per Reel and Reel Size

<table>
<thead>
<tr>
<th>Package</th>
<th>Carrier Width (W)</th>
<th>Pitch (P)</th>
<th>Part Per Full Reel</th>
<th>Reel Size</th>
</tr>
</thead>
<tbody>
<tr>
<td>16-Pin SOIC (W)</td>
<td>16 mm</td>
<td>12 mm</td>
<td>1000</td>
<td>13 in</td>
</tr>
</tbody>
</table>

5.3 Package Dimensions

8-Pin CDIP (Narrow)

Dimensions: inches (mm)
8-Pin Plastic DIP

Dimensions: inches (mm)

16-Pin SOIC (Wide)

Dimensions: inches (mm)
THE MICROCHIP WEB SITE

Microchip provides online support via our WWW site at www.microchip.com. This web site is used as a means to make files and information easily available to customers. Accessible by using your favorite Internet browser, the web site contains the following information:

- **Product Support** – Data sheets and errata, application notes and sample programs, design resources, user’s guides and hardware support documents, latest software releases and archived software
- **General Technical Support** – Frequently Asked Questions (FAQ), technical support requests, online discussion groups, Microchip consultant program member listing
- **Business of Microchip** – Product selector and ordering guides, latest Microchip press releases, listing of seminars and events, listings of Microchip sales offices, distributors and factory representatives

CUSTOMER CHANGE NOTIFICATION SERVICE

Microchip’s customer notification service helps keep customers current on Microchip products. Subscribers will receive e-mail notification whenever there are changes, updates, revisions or errata related to a specified product family or development tool of interest.

To register, access the Microchip web site at www.microchip.com, click on Customer Change Notification and follow the registration instructions.

CUSTOMER SUPPORT

Users of Microchip products can receive assistance through several channels:

- Distributor or Representative
- Local Sales Office
- Field Application Engineer (FAE)
- Technical Support
- Development Systems Information Line

Customers should contact their distributor, representative or field application engineer (FAE) for support. Local sales offices are also available to help customers. A listing of sales offices and locations is included in the back of this document.

**Technical support is available through the web site at:** http://support.microchip.com
READER RESPONSE

It is our intention to provide you with the best documentation possible to ensure successful use of your Microchip product. If you wish to provide your comments on organization, clarity, subject matter, and ways in which our documentation can better serve you, please FAX your comments to the Technical Publications Manager at (480) 792-4150.

Please list the following information, and use this outline to provide us with your comments about this document.

To: Technical Publications Manager

RE: Reader Response

From: Name ____________________________

Company ________________________________

Address ________________________________

City / State / ZIP / Country ________________________________

Telephone: (_____) _________ - _________

FAX: (_____) _________ - _________

Application (optional): ________________________________

Would you like a reply? ___ Y ___ N

Device: TC962

Literature Number: DS21484C

Questions:

1. What are the best features of this document?

_____________________________________________________________________________________

2. How does this document meet your hardware and software development needs?

_____________________________________________________________________________________

3. Do you find the organization of this document easy to follow? If not, why?

_____________________________________________________________________________________

4. What additions to the document do you think would enhance the structure and subject?

_____________________________________________________________________________________

5. What deletions from the document could be made without affecting the overall usefulness?

_____________________________________________________________________________________

6. Is there any incorrect or misleading information (what and where)?

_____________________________________________________________________________________

7. How would you improve this document?

_____________________________________________________________________________________
Note the following details of the code protection feature on Microchip devices:

- Microchip products meet the specification contained in their particular Microchip Data Sheet.
- Microchip believes that its family of products is one of the most secure families of its kind on the market today, when used in the intended manner and under normal conditions.
- There are dishonest and possibly illegal methods used to breach the code protection feature. All of these methods, to our knowledge, require using the Microchip products in a manner outside the operating specifications contained in Microchip’s Data Sheets. Most likely, the person doing so is engaged in theft of intellectual property.
- Microchip is willing to work with the customer who is concerned about the integrity of their code.
- Neither Microchip nor any other semiconductor manufacturer can guarantee the security of their code. Code protection does not mean that we are guaranteeing the product as “unbreakable.”

Code protection is constantly evolving. We at Microchip are committed to continuously improving the code protection features of our products. Attempts to break Microchip’s code protection feature may be a violation of the Digital Millennium Copyright Act. If such acts allow unauthorized access to your software or other copyrighted work, you may have a right to sue for relief under that Act.

Information contained in this publication regarding device applications and the like is provided only for your convenience and may be superseded by updates. It is your responsibility to ensure that your application meets with your specifications. MICROCHIP MAKES NO REPRESENTATIONS OR WARRANTIES OF ANY KIND WHETHER EXPRESS OR IMPLIED, WRITTEN OR ORAL, STATUTORY OR OTHERWISE, RELATED TO THE INFORMATION, INCLUDING BUT NOT LIMITED TO ITS CONDITION, QUALITY, PERFORMANCE, MERCHANTABILITY OR FITNESS FOR PURPOSE. Microchip disclaims all liability arising from this information and its use. Use of Microchip devices in life support and/or safety applications is entirely at the buyer's risk, and the buyer agrees to defend, indemnify and hold harmless Microchip from any and all damages, claims, suits, or expenses resulting from such use. No licenses are conveyed, implicitly or otherwise, under any Microchip intellectual property rights.

Trademarks
The Microchip name and logo, the Microchip logo, Accuron, dsPIC, KEELoc, microID, MPLAB, PIC, PICmicro, PICSTART, PRO MATE, PowerSmart, rPIC, and SmartShunt are registered trademarks of Microchip Technology Incorporated in the U.S.A. and other countries.

AmpLab, FilterLab, Migratable Memory, MXDEV, MXLAB, SEEVAL, SmartSensor and The Embedded Control Solutions Company are registered trademarks of Microchip Technology Incorporated in the U.S.A.

Analog-for-the-Digital Age, Application Maestro, dsPICDEM, dsPICDEM.net, dsPICworks, ECAN, ECONOMONITOR, FanSense, FlexROM, fuzzyLAB, In-Circuit Serial Programming, ICSP, ICEPIC, Linear Active Thermistor, Mindi, MiWi, MPASM, MPLIB, MPLINK, PICkit, PICDEM, PICDEM.net, PICLAB, PICtail, PowerCal, PowerInfo, PowerMate, PowerTool, REAL ICE, rLAB, rPICDEM, Select Mode, Smart Serial, SmartTel, Total Endurance, Uni/O, WiperLock and ZENA are trademarks of Microchip Technology Incorporated in the U.S.A. and other countries.

SQTP is a service mark of Microchip Technology Incorporated in the U.S.A.

All other trademarks mentioned herein are property of their respective companies.

© 2006, Microchip Technology Incorporated, Printed in the U.S.A., All Rights Reserved.

Printed on recycled paper.

QUALITY MANAGEMENT SYSTEM
CERTIFIED BY DNV

ISO/TS 16949:2002

Microchip received ISO/TS-16949:2002 certification for its worldwide headquarters, design and wafer fabrication facilities in Chandler and Tempe, Arizona, Gresham, Oregon and Mountain View, California. The Company's quality system processes and procedures are for its PICmicro® 8-bit MCUs, KEELoc® code hopping devices, Serial EEPROMs, microperipherals, nonvolatile memory and analog products. In addition, Microchip's quality system for the design and manufacture of development systems is ISO 9001:2000 certified.

© 2006 Microchip Technology Inc.
### AMERICAS
- **Corporate Office**
  - 2355 West Chandler Blvd.
  - Chandler, AZ 85224-6199
  - Tel: 480-792-7200
  - Fax: 480-792-7277
  - Technical Support: http://support.microchip.com
  - Web Address: www.microchip.com
- **Asia Pacific Office**
  - Suites 3707-14, 37th Floor
  - Tower 6, The Gateway
  - Harbour City, Kowloon
  - Hong Kong
  - Tel: 852-2401-1200
  - Fax: 852-2401-3431
- **Europe**
  - **Austria - Wels**
    - Tel: 43-7242-2244-3910
    - Fax: 43-7242-2244-393
  - **Denmark - Copenhagen**
    - Tel: 45-4450-2828
    - Fax: 45-4485-2829
  - **France - Paris**
    - Tel: 33-1-69-53-63-20
    - Fax: 33-1-69-30-90-79
  - **Germany - Munich**
    - Tel: 49-89-627-144-0
    - Fax: 49-89-627-144-44
  - **Italy - Milan**
    - Tel: 39-0331-742611
    - Fax: 39-0331-466781
  - **Netherlands - Drunen**
    - Tel: 31-416-690399
    - Fax: 31-416-690340
  - **Spain - Madrid**
    - Tel: 34-91-708-08-90
    - Fax: 34-91-708-08-91
  - **UK - Wokingham**
    - Tel: 44-118-921-5869
    - Fax: 44-118-921-5820

### ASIA/PACIFIC
- **Australia - Sydney**
  - Tel: 61-2-9868-6733
  - Fax: 61-2-9868-6755
- **China - Beijing**
  - Tel: 86-10-8528-2100
  - Fax: 86-10-8528-2104
- **China - Chengdu**
  - Tel: 86-28-8676-6200
  - Fax: 86-28-8676-6599
- **China - Fuzhou**
  - Tel: 86-591-8750-3506
  - Fax: 86-591-8750-3521
- **China - Hong Kong SAR**
  - Tel: 852-2401-1200
  - Fax: 852-2401-3431
- **China - Qingdao**
  - Tel: 86-532-8502-7355
  - Fax: 86-532-8502-7205
- **China - Shanghai**
  - Tel: 86-21-5407-5533
  - Fax: 86-21-5407-5066
- **China - Shenyang**
  - Tel: 86-24-2334-2829
  - Fax: 86-24-2334-2393
- **China - Shenzhen**
  - Tel: 86-755-8203-2660
  - Fax: 86-755-8203-1760
- **China - Shunde**
  - Tel: 86-757-2839-5507
  - Fax: 86-757-2839-5571
- **China - Wuhan**
  - Tel: 86-27-5980-5300
  - Fax: 86-27-5980-5118
- **China - Xian**
  - Tel: 86-29-8833-7250
  - Fax: 86-29-8833-7256

### EUROPE
- **Austria - Wels**
  - Tel: 43-7242-2244-3910
  - Fax: 43-7242-2244-393
- **Denmark - Copenhagen**
  - Tel: 45-4450-2828
  - Fax: 45-4485-2829
- **France - Paris**
  - Tel: 33-1-69-53-63-20
  - Fax: 33-1-69-30-90-79
- **Germany - Munich**
  - Tel: 49-89-627-144-0
  - Fax: 49-89-627-144-44
- **Italy - Milan**
  - Tel: 39-0331-742611
  - Fax: 39-0331-466781
- **Netherlands - Drunen**
  - Tel: 31-416-690399
  - Fax: 31-416-690340
- **Spain - Madrid**
  - Tel: 34-91-708-08-90
  - Fax: 34-91-708-08-91
- **UK - Wokingham**
  - Tel: 44-118-921-5869
  - Fax: 44-118-921-5820

06/08/06