Product Description

LPC Firmware Flash/Firmware Hub is a family of flash memories designed for system code and data storage. SST LPC (Low Pin Count) Flash and Firmware Hub Flash complies with the Intel LPC Interface specification supporting multi-byte Firmware Memory and single-byte LPC Memory cycle types. The low pin count interface allows ASIC host controllers to have more free pins resulting in lower overall system costs.

LPC Firmware Flash/Firmware Hub products are ideally suited for embedded systems and processor applications. Available in an array of package and density combinations, this family of devices also offers all of the benefits of SuperFlash® technology: high performance, low power and reliability.

Key Features

Intel Support
- Compliant with Intel Low Pin Count (LPC) Interface Specification 1.1
- Compatible with Intel 82802 Firmware Hub (FWH) component

High Bandwidth
- Up to 33 MHz operating frequency
- Parallel Programming (PP) for fast production programming
- Unique features for 16 Mbit devices
  - 128 byte burst programming
  - Up to 15.6 MB/s data transfer rate
  - Auto Address Increment (AAI)

Protection/Security
- 256 bit secure ID space
- Boot block options
- Hardware write protection

SuperFlash Technology
- Endurance, 100,000 cycles (typical)
- Greater than 100 years data retention
- Fast Sector Erase and Block Erase time:
  25 ms (max)
- Low power

Applications

- Embedded System Firmware
- Medical
- Industrial Control
- PC BIOS
- Communications Infrastructure
- Optical Drives
- Print Imaging
- Military Equipment

The 49 Series family enables BIOS and system code storage in applications such as embedded PCs, industrial controls, gaming machines, interactive clients, infotainment and medical devices where high bandwidth and a low pin count are required.
### 49 Series: LPC/Firmware Hub Flash

<table>
<thead>
<tr>
<th>Device</th>
<th>Density</th>
<th>Voltage</th>
<th>Access Frequency (MHz)</th>
<th>Operating Temperature</th>
<th>Operational Mode</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>SST49LF008A</td>
<td>8 Mbit</td>
<td>3.0-3.6V</td>
<td>33</td>
<td>0°C to 85°C</td>
<td>FWH/PP</td>
<td>PLCC-32, TSOP-32, TSOP-40</td>
</tr>
<tr>
<td>SST49LF080A</td>
<td>8 Mbit</td>
<td>3.0-3.6V</td>
<td>33</td>
<td>0°C to 85°C</td>
<td>LPC/PP</td>
<td>PLCC-32, TSOP-32</td>
</tr>
<tr>
<td>SST49LF016C</td>
<td>16 Mbit</td>
<td>3.0-3.6V</td>
<td>33</td>
<td>0°C to 85°C</td>
<td>FWH/AAI</td>
<td>PLCC-32, TSOP-32, TSOP-40</td>
</tr>
<tr>
<td>SST49LF160C</td>
<td>16 Mbit</td>
<td>3.0-3.6V</td>
<td>33</td>
<td>0°C to 85°C</td>
<td>LPC/AAI</td>
<td>PLCC-32</td>
</tr>
</tbody>
</table>

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### Package Diagram PLCC-32 (NHE)

![PLCC-32 Package Diagram](image1)

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### Package Diagram TSOP-32 (WHE)

![TSOP-32 Package Diagram](image2)

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### Package Diagram TSOP-40 (EIE)

![TSOP-40 Package Diagram](image3)

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### Legacy-Free PC Systems

![Legacy-Free PC System Diagram](image4)

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