Advanced MPF+ is a family of high-performance, parallel flash memories that feature data protection capabilities as well as fast program, erase and read times. These high-speed operations enable quick boot times and field updates, resulting in better end-user experiences. These capabilities also provide higher throughput in manufacturing lines that use in-system programming of the flash. The data protection features of the Advanced MPF+ family prevent unauthorized data changes, ensuring that content stored on the flash memory is secure.

The 38 Series Advanced MPF+ products are ideal for applications that need high performance or require integrity of data and services. Available in standard packages with compatible pinouts, this family of devices also offers all the benefits of SuperFlash® technology: high-performance, low power and reliability.

### Key Features

**Page Read**
- 25 ns/word (max) page read access time
- 4 word page read buffer

**Write-Buffer Programming**
- 2.5 μs/word (max) programming time
- (full buffer)
- 16 word write buffer

**Bypass Mode**
- Reduce overhead of program and erase commands

**Protection/Security**
- 128-bit unique ID
- 256 word user programmable SEC ID area
- Individual block protection
- Irreversible block lock
- Boot-block options

**SuperFlash Technology**
- Endurance, 100,000 cycles (min)
- Greater than 100 years data retention
- Fast Sector Erase and Block Erase time: 25 ms (max)
- Low power

### Applications

- Set-top Boxes
- Digital TVs
- DSL/Cable Modems
- Digital Camcorders
- Multi-Function Printers
- Digital Cameras
- WiFi/WiMAX
- Digital Video Recorders
- Servers
- Industrial

### Advanced MPF+ Benefits

**Page Read** – Short boot-up times and fast execute-in-place

**Write-Buffer Programming** – Fast field updates and in-system programming

**Bypass Mode** – Reduces overhead on program and erase commands

**Protection/Security** – Prevents unauthorized and accidental changes to data

**SuperFlash Technology** – High performance, superior reliability and low power
<table>
<thead>
<tr>
<th>Device</th>
<th>Density</th>
<th>Voltage</th>
<th>Access Speed (ns)</th>
<th>Temperature</th>
<th>Hardware Block Protection</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>SST38VF6401</td>
<td>64 Mbit</td>
<td>2.7-3.6V</td>
<td>90</td>
<td>C/I</td>
<td>Bottom boot-block uniform (32 Kword)</td>
<td>TSOP-48, TFBGA-48</td>
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<td>2.7-3.6V</td>
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<td>C/I</td>
<td>Top boot-block non-uniform (8 Kword)</td>
<td>TSOP-48, TFBGA-48</td>
</tr>
</tbody>
</table>

**Functional Block Diagram**

**Package Diagram TSOP-48 (EK)**

**Package Diagram TFBGA-48 (B3K)**