MOST® Network Interface Controller
Multimedia Network Protocol Chip OS8104A

Summary
The OS8104SA is a highly integrated C-MOS Network Interface Controller with a full-featured interface to the 24.5 Mbit/s MOST optical network system.

All relevant network management on the data link layer level is on-chip, including stream data allocation, synchronization, and network detection. An ultra-low jitter PLL guarantees high-quality audio and video transmission and clock recovery over a wide frequency range. Coding is optimized for plastic optical fiber.

The OS8104A works at an ultra-low bit error rate supporting real-time synchronous data traffic of up to 15 stereo channels simultaneously. In addition to the control channel, some of the available bandwidth can be allocated to asynchronous packet data transfer.

Programmable serial and parallel real-time data interfaces with different clock and data modes support various consumer electronic devices such as codecs, converters and DSPs.

Enhanced Features
- A value-priced option to the OS8104
- Power consumption is reduced by 55% to 432 mW as compared to the OS8104 controller
- 3.3V power supply, inputs tolerant of 5.0V signals
- Variable retry function
- Automatic channel muting
- Enhanced bypass architecture
- Increased system jitter tolerance
- Frequency regulator allows the PLL to lock to a stable reference clock when not locked to the network
- Max. zero-power-mode supply current reduced to 1 mA
- Max. digital supply current reduced to:
  - 100 mA in normal operation
  - 40 mA in low-power mode
- Extended temperature range
  - Standard range: −40 to +85°C
  - Extended range: −40 to +95°C

Legacy Features
Multimedia Network Solution
- 24.5 Mbps MOST Network Interface Controller
- Support for up to 60 bytes per frame of streaming data
- Support for 11 Mbps packet data
- Max. packet data size: 1014 bytes
- Support for up to 968 control messages per second
- Support for 256 xFs and 384 xFs crystal oscillator

On-Chip Network Management
- Stream data allocation
- Network activity and valid detection
- Node position and delay sensing, alive check and power management

Flexible Consumer Electronics Interface
- Parallel, I²C™ and SPI control interface
- S/PDIF: IEC60958 digital interface
Functional Description

The OS8104A contains a MOST core and several peripherals including a clock manager, source data ports, a control port, and a network interface. The network interface includes an ultra-low jitter phase lock loop (PLL) and a channel emodulator on the input. The network output provides a channel coded signal. These signals can be connected directly to an optical receiving/transmitting device (FOT unit) or a balanced line driver.

Various ADCs, DACs, digital signal processors and media players (CD-Audio/Video/DVD etc.) can be synchronized using one of the many clock and serial interface modes. The source data ports provide a data interface to the serial bit stream on the MOST network. Data sources and sinks can be linked to either a serial or parallel source port in order to send or receive data to/from another node on the network. The OS8104A operates in a peer-to-peer network or host-centric architecture.