INTRODUCTION

This application note covers the general concept, design and some source code modifications for using the MCP1631HV Digitally Controlled Programmable Current Source Reference Design for an LED lighting application. The document offers additional insight into the MCP1631HV Digitally Controlled Programmable Current Source Reference Design. A Microchip MCP1631 PWM controller and a low-cost PIC16F616 microcontroller are used to manage a SEPIC (Single Ended Primary Inductive Converter) powertrain. The SEPIC topology is then used to drive and dim Light Emitting Diodes (LEDs).

The LED technology continues to change and evolve. The energy efficiency (lumens/watt) of today’s LEDs allows them to replace the classical tungsten filament and gas vapor lighting systems with solid-state technology. The most efficient LEDs are able to perform as well as Compact Fluorescent Lamps (CFL). The most important advantage of LEDs over tungsten and gas vapor bulbs is the cycle life. LED lifetimes currently show a 30% improvement over their counterparts. LEDs are being used in the automotive and commercial lighting industries with good results.

LEDs must be carefully integrated into lighting systems because of their sensitivity to thermal and electrical stress. The LED current must be optimized for both thermal and electrical characteristics. Excessive driving current will deteriorate LED performance, shorten the lifetime, and cause permanent reduction in the luminous intensity.

LEDs may be driven using constant current sources in order to maintain a consistent color output. One low cost but inefficient solution for driving LEDs is using series resistors. The series resistor method is very sensitive to power supply variation. Constant LED current depends on a constant supply voltage. Any variation of the voltage supplying the resistor and LED combination will result in a change in current and thus a change in output color. There are more efficient solutions for driving LEDs that save energy and provide good current control. Two methods of driving an LED are Constant Current and Pulse Width Modulation (PWM) current control.

The PWM driver is used to dim an LED and is based upon the persistence of vision of the human eye. The current does not flow through the LED continuously. The PWM period is normally in the range of 100 Hz to 250 Hz. Dimming is obtained by changing the PWM duty cycle. Variations in supply voltage will directly affect the LED current.

The Constant Current Source driver is used to supply the LED with a constant current. The constant current results in a consistent LED color output. A high frequency PWM signal may be used to control the current that is flowing through the LED. The PWM signal is used to set the LED current and thus the LED intensity.

The Constant Current Source driver is based upon driving the LED with a constant current source to obtain the desired brightness and color. The current source is controlled by a high frequency PWM signal. The PWM modulated constant current source is set to the desired LED current. By changing the PWM duty cycle, the LED current can be adjusted for desired intensity and to offset aging.

Constant current driving is recommended to eliminate light flickering and electrical stress due to voltage fluctuations. An important observation is the LED is not affected by the variation of input power supply voltage that can vary as much as +/- 30% of the nominal value.

The design topology used for this application note will be the Single Ended Primary Inductive Controller or SEPIC topology. The SEPIC topology allows both buck and boost operation. This allows for a constant current drive output with variations in input voltage. The SEPIC uses a current sense feedback loop for efficient control. Microchip’s MCP1631HV Digitally Controlled Programmable Current Source Reference Design is used for this application note.
LED BACKGROUND

There are a few important parameters that must be considered when designing an LED driver.

LED Power, Maximum Forward Current and Voltage

Generally, the LED manufacturers recommend driving LEDs at constant current. For power type LEDs, the drive current covers hundreds of milliamps. Standard values which are currently used for LED drive current are: 350 mA, 700 mA, 1A and higher. Some standard power LEDs are rated for 1W, 3W, and 5W. LED Power is $V_f \times I_f$, where $V_f$ is the forward voltage of the LED and $I_f$ is the forward current.

Power LED applications utilizing higher current levels waste energy and create excessive heat when supplied by series resistors. Any power supply voltage variation will result in a change in LED current and luminous intensity. It is important to note that the LED’s forward voltage varies from device to device and also varies significantly with temperature.

![FIGURE 1: Typical Forward Current vs. Forward Voltage for a White, Blue, Green, and Amber Power LEDs (P3 series) at +25°C.](image)

The Direct Relation between Forward Voltage and the Color of the LED, Maximum Reverse Voltage

Different color LEDs have different forward voltage ($V_f$) drops based upon the diode chemistry. A red LED dissipates less energy than a white LED when operated at the same current level. That is because the red LED has a lower Forward Voltage drop than the white LED. LED manufacturers datasheets will provide the maximum forward voltage drop ($V_{f_{max}}$) at a given maximum forward current ($I_{f_{max}}$). The higher $V_{f_{max}}$ values are typically for white and blue LEDs and the lower are typically for red LEDs.

The typical forward voltage drop of a red LED (ultra red, super red, or amber) ranges from 1.8V to 2.2V. Yellow and green LEDs range from 2.1V to 2.2V. Blue and white (cool or warm) LEDs range from 3.6V to 3.8V. The user should consult the manufacturers data sheet for the proper design parameters when using high power LEDs.

Another important parameter is the Maximum Reverse Voltage ($V_{r_{max}}$), which is typically 5V. Applying reverse voltage in excess of the manufacturers recommendation will degrade the LED and may destroy it.

Series and Parallel Connection

If the user intends to connect two or more power LEDs in series, it is necessary to calculate the sum of the individual forward voltages. The color of the LEDs used may be different when using a series topology.

![FIGURE 2: Topology of an LED String (Series and Parallel Connection).](image)

The LED string can be driven by a Constant Current driver without series resistors. The sum of the forward voltage of each diode in a series string must not exceed the maximum voltage capability of the current source. Due to LED tolerances, parallel connections using a single current sense may not be able to maintain a uniform intensity and color. Better control is done using separate current sensing for each LED rung in a parallel system.

The advantage of paralleling LEDs is that more LEDs can be supplied at lower output voltage levels on constant current supplies. If slight variations in luminous intensity are tolerable, a single current sense can be used to control the current loop. However, if one rung of the parallel LED string fails, the current through the other LEDs will increase and possibly cause undesirable consequences. Therefore, it is recommended to monitor current through each rung and drive each rung as if the string was a series string.
When LEDs are connected in series, the maximum reverse voltage increases with each additional LED added to the string. The group of LEDs may make up a series connected system or they may make up one rung of a parallel connected system.

**Operating Temperature and Cooling the Power LED**

The forward voltage of an LED is temperature dependent. The temperature coefficient of forward voltage is published in the data sheet and its value is around 3 to 5 mV/°K, depending on the LED’s color. This parameter shows the changes in forward voltage with the ambient temperature. The temperature increases directly with forward current. The Operating Temperature Range is generally between -20°C and +85°C. It is important to keep the LED case temperature around 30°C to 40°C for good results. The LED junction can typically work at a maximum of +125°C.

The LED manufacturers recommend in their datasheets that a heatsink be used with the power LEDs. A successful design starts with the transfer of LED heat away from the device. Two typical methods used to provide proper heat sinking are an external heatsink or the printed circuit board (PCB) layout.

A PCB on FR4 substrate with a large copper area and thermal vias under a surface mount (SMD) LED is a low-cost but inefficient solution. For good thermal results, the PCB needs to be double-sided. PCB heat-sinking is not indicated for power LEDs that require more than 1W. Modern lighting lamps use Metal Core PCBs (MCPCB) with special substrates like aluminum or copper. Copper offers the best thermal conductivity. These PCBs offer better cooling because of the lower thermal resistance of the core metal compared to FR4 fiberglass. The heat is dissipated in all directions on the metal surface. Where the space does not permit a large heat sink or a large metal PCB, a small electric fan may be used to force the air across the heat sink.

It is recommended to use extra thermal protection devices to avoid accidental overheating due to wrong drive current, high ambient temperature or fan fault. The extra protection can be managed by an electronic controller. A small NTC thermistor or a semiconductor sensor may be placed on the LED heat sink or on the metal PCB. The temperature trip point may be set around 30°C to 40°C.

Note that the short term effect of excessive heating is color shifting, which is reversible. The long term effect of excessive heating is a permanent decrease in luminous intensity and LED life cycle. The ideal operating temperature is typically 25°C. The luminous intensity will be at a maximum around this temperature.

The red and amber LEDs are affected the most by heating. The white LED is least affected by heating.

**Minimum Heat Sink Requirements, Thermal Modeling**

When designing a lighting system with power LEDs, a big challenge is thermal management. Thermal management includes the selection of the cooling method or combination of methods. Thermal management methods may consist of PCB material, fan, and heat sinks and their respective fastening processes. After selecting the cooling method, the cooling system may then be designed.

Thermal modeling is used to predict the junction temperature of the LED in order to avoid exceeding the LED thermal limits specified in the datasheet. The thermal limit for power LEDs is typically between +120°C and +135°C.

The thermal model used in thermal management design is the concept of Thermal Resistance (Rt), given in °C/W. Thermal resistance is defined as the ratio of temperature difference to the corresponding electrical power dissipation.

**EQUATION 1:**

$$R_{tJA}[{^oC/W}] = \Delta T_{JA}/P_{dLED}$$

Where:

- $R_{tJA} =$ represents the thermal resistance from junction to ambient.
- $P_{dLED} =$ the electrical power dissipated by LED.
- $\Delta T_{JA} =$ the difference between junction and ambient temperatures.

**EQUATION 2:**

$$P_{dLED}[W] = V_f \times I_f$$

Where:

- $V_f =$ the forward voltage.
- $I_f =$ the forward current.

**EQUATION 3:**

$$\Delta T_{JA}[{^oC}] = T_J - T_A$$

Where:

- $T_J =$ the junction temperature.
- $T_A =$ the ambient temperature.

LED manufacturers typically specify in their data sheets the thermal resistance from the diode junction to the ambient $R_{tJA}$ (solder point or LED case).

Thermal resistances $R_{tJC}$ (Thermal Resistance from junction to case) and $R_{tCA}$ (Thermal Resistance from case to air) should be optimized for each application.
Since $R_{\text{JUC}}$ is determined by the device construction, only $R_{\text{JCA}}$ may be optimized for thermal efficiency. $R_{\text{JUC}}$ typically ranges from $3^\circ \text{C/W}$ to $4^\circ \text{C/W}$, depending on the LED manufacturer. Increasing the heat sink surface area will improve the heat dissipation.

The typical thermal model of an LED package consists of:

- LED power dissipation (Equation 2) which is modeled as a current source
- thermal resistance which is modeled as a resistor (Equation 1)
- the difference between ambient temperature and junction temperature which is modeled as a voltage source (Equation 3)

These are solved by Ohm's law following the model in Figure 3. The working junction temperature, $T_J$, must be kept lower than the $T_{\text{Jmax}}$ of the LED.

\[ P_d = V_f \times I_f \]

\[ R_{\text{JA}} = R_{\text{JUC}} + R_{\text{JCA}} \]

\[ R_{\text{JA}} = R_{\text{JUC}} + R_{\text{ICS}} + R_{\text{ISA}} \]

Mounting a power LED ensemble on a heat sink may require a thermal adhesive such as tape or thermal grease. Thermal adhesive tape is recommended for wide and flat surfaces. Avoid any air pockets between the thermal adhesive and the attached face when applying the tape.

There is a dependence between the working junction temperature and the PCB or heat sink thickness. Thicker metal provides a lower working junction temperature. This dependence on thickness can be seen in Figure 4 for a typical 1W P3 series LED.

**Luminous Intensity. Viewing Angle**

The unit of measurement commonly used to describe the LED intensity is the millicandela (mcd).

1000 mcd = 1 candela.

Part of the SI system of measurement, one candela (cd) is the monochromatic radiation of $540 \times 10^{12}$ Hz which has a radiant intensity of 1/683 Watt per steradian in the same direction. The unit of measurement commonly used for most other light sources is the Lumen. Lumens are units of Luminous Flux. The Luminous Flux (lumens) from a light source is equal to the Luminous Intensity (candelas) multiplied by the solid angle over which the light is emitted.

The solid angle is the angle (here defined in 3D space) that an object subtends at a point. It is a measure of how big an object appears to an observer who is looking from that object. The Lumens measure how much light actually falls on a surface.

Two optical parameters that are very important when we choose a power LED for a lighting application are the luminous intensity and the viewing angle. It may take some extra calculations to determine which manufacturer’s power LED is right for your application, as each manufacturer tends to test their LEDs under different operating conditions. For lighting applications, an LED with a 100° to 130° viewing angle is typically recommended. Lumens per watt (lm/W) is a measure of efficiency in converting electrical energy to light. Multiply this by the watts dissipated in the LED to get lumens.
POWER LED DIMMING SOLUTIONS

There are applications that have the requirement to control the luminous intensity of LEDs. The modalities for adjusting the brightness of LEDs were summarized in the previous section:
• Adjusting the forward current value
• Dimming by Pulse-Width-Modulation (PWM)

Adjusting the Forward Current Value

Generally, there are two methods to drive power LEDs by constant current. One method is to use a constant current source and the other method is to use constant voltage source. The constant current source method can drive the LED with a programmed current at the expense of a few more circuit devices. Dimming is obtained by modifying the value of current supplied by the current source.

The constant voltage source method is simply connecting the LED to the constant voltage power supply in series with a resistor chosen for the desired current. It is the simplest and lowest-cost solution and thus a popular way to drive LEDs when efficiency and color range are not critical. Dimming would require either stepping the input voltage or switching different resistor taps to obtain a new current through the LED.

From an efficiency and performance standpoint, the best choice is a current source that has the ability to generate digitally programmable current levels as needed. A digitally programmable current source may use a current sense resistor in the control loop to monitor the LED current. The current sense would be used by the control loop to maintain regulation about the programmed current setpoint. The current sense resistor is part of the output loop in a SEPIC topology setup. The resistor senses the loop current of the SEPIC topology consisting of the secondary coil, rectifier and LED. The loop current of the SEPIC topology consisting of the primary coil, switch and source power supply is sensed by a separate resistor. The primary sense monitors peak current in the primary loop. The secondary sense monitors average current in the secondary loop. All these key features are covered by the MCP1631HV Digitally Controlled Programmable Current Source Reference Design.

Note that the relative luminosity is directly related to the driving current which is a quasi-constant linear function. The dimming method by variable current control has the disadvantage of a shift in color as the current level is changed. Most shift of color is around base color and under certain circumstances may be tolerated. The color of an LED at rated forward current will be different than a dimmed LED operating at 50% of rated forward current.

Dimming by PWM

As explained in the previous section, luminous intensity can be modified by controlling the current to the LED. However, this causes a color shift that for some LED applications is not tolerated. The PWM control changes the luminous intensity without changing the color of the LED. PWM control consists of maintaining a constant current setpoint value to maintain LED color while varying the duty cycle to control the LED brightness. The forward current through the LED is kept at a constant value and only the duty cycle (D) is changed. When the current is flowing through the LED during the “ON” portion of the duty cycle, the LED color operating point is set. When the current flow is cut off for the “OFF” portion of the duty cycle, the LED is also off. The persistence of the human eye causes the appearance of brightness changes relative to duty cycle. The duty cycle expresses the ratio between pulse “ON” duration (tPULSE) and the pulse period (T).

EQUATION 5:

\[ D = \frac{t_{\text{PULSE}}}{T} \]

The human eye typically does not notice the variation in light above 120 Hz. The eye integrates and interprets the light pulses in terms of brightness that can be changed by varying the duty cycle of the source.

PWM control also reduces electrical power consumption because no series voltage dropping resistors are required. The brightness of the LED may also be changed linearly by varying the PWM duty cycle. The drawbacks of the PWM method are that it may produce switching spikes and the square pulses may electrically stress the die.
MCP1631 PWM CONTROLLER

The MCP1631/MCP1631V is a high-speed pulse width modulator (PWM) used to develop intelligent power systems. When combined with a microcontroller, the MCP1631/MCP1631V will control the powertrain duty cycle to provide output voltage or current regulation. A functional block diagram of the PWM controller is shown in Figure 7.

The MCP1631/MCP1631V inputs were developed to be attached to the I/O pins of a microcontroller. By combining the MCP1631/MCP1631V with a low cost microcontroller, intelligent LED lighting designs can be easily developed.

The microcontroller is used to adjust the output current, switching frequency and maximum duty cycle. It provides additional features that make the power system more intelligent, robust and adaptable.

Additional features integrated into the MCP1631HV/MCP16311HV provide signal conditioning and protection features for constant current source applications.

The MCP1631 is available in a 20-Lead 4x4 mm QFN, 20-Lead TSSOP, or SSOP packages.

Powering the MCP1631

For applications that operate from a high voltage input, the MCP1631HV and MCP16311HV device options may be used. They will operate directly from a +3.5V to +16V input. For these applications, an additional low drop out +5V or +3.3V regulated output is available on chip and can provide up to 250 mA of current to power a microcontroller and auxiliary circuits.

High-Speed Analog PWM Operation

The high-speed analog PWM is used to drive the SEPIC powertrain switch to regulate the output of the converter. Voltage or current may be regulated depending on what is being sensed. For the SEPIC LED driver application, the MCP1631HV is always regulating current. A PIC microcontroller is providing the oscillator and current references required to obtain the desired output current.

The PWM signal starts with the PIC generated oscillator input. When the oscillator input is low, the V\text{EXT} driver output pin is pulled high, turning on the external N-Channel MOSFET switch.

The current begins to ramp up in the primary coil current sense resistor until the voltage exceeds 1/3 of the V\text{REF} reference value supplied by the microcontroller. The V\text{REF} voltage is internally limited to 0.9V. The 0.9V limit is used as an overcurrent limit. A passive filter is used on the current sense input (CS) to remove the leading edge turn-on spike associated with the turn-on of the external power MOSFET. The MCP1631/MCP1631V internal P-Channel MOSFET output driver is powered using a separate P\text{VDD} pin to keep switching noise off of the analog A\text{VDD} pin and sensitive current sense circuitry.

The error amplifier (A1 on datasheet diagram) is used to compare the switch current with the programmed reference current. When the switch current is less than the programmed current, the PWM switch driver duty cycle increases. When the switch current is greater than the programmed current, the PWM switch driver duty cycle decreases. The PWM duty cycle will track changes in output current and adjust the duty cycle accordingly. The external feedback (FB) and compensation (COMP) pins are used to control the speed of the error amplifiers output response.

The MCP1631HV contains a 10V/V inverting gain amplifier (A2) for general purpose use. The A2 current sense amplifier may be used to amplify the sense current in the secondary side of the SEPIC converter or freewheeling current in a Buck converter.

Sensing Battery Voltage

Using the microcontroller A/D converter to sense load voltage requires a low source impedance to perform accurate readings. Low source impedance requires low resistance values that would draw excessive current at the converter output, decreasing the efficiency. The MCP1631HV integrates another amplifier (A3) configured as a unity gain buffer. The buffer provides a high impedance input to the voltage sense divider while also providing a low impedance source for the microcontroller A/D converter input.
Overvoltage Protection

Overvoltage (OV) protection is a common voltage converter protection feature. It is used to protect the power switch from excessive voltage if the load is removed. OV protection also prevents accidental overvoltage at the load if the programmed output current exceeds the absorption capability of the load. OV protection is typically required for any current source application such as battery chargers and LED drivers.

The MCP1631HV integrates an internal high speed OV comparator (C2) that uses a 1.2V reference. When the voltage on the OV_IN pin exceeds the 1.2V reference threshold, the V_EXT output is immediately driven low. The V_EXT output will be re-enabled once the OV_IN voltage has dropped below the 1.2V reference by 50 mV. The 50 mV is the typical hysteresis of the OV comparator.

OSC Disable Feature

The oscillator disable input (OSC_DIS) is used to asynchronously terminate the PWM V_EXT output. This can be used along with the shutdown (SHDN) pin and a separate PWM input to modulate current into an LED for lighting applications.
FIGURE 7: MCP1631 PWM Controller - Functional Block Diagram.

Note 1: On Shutdown, amplifier A3 remains functional.

2: For HV options, internal Low Drop Out Regulator provides +3.3V or +5.0V bias to VDD.
Shutdown Feature

The MCP1631/MCP1631V shutdown (SHDN) feature is used to disable the device and to minimize the quiescent current draw. While shutdown, amplifier A3 remains operational. During shutdown, the device typically draws 4.4 μA from the supply source.

UnderVoltage Lockout and Thermal Shutdown

The MCP1631/MCP1631V has built-in UnderVoltage Lockout (UVLO) that ensures the output V_EXT pin is forced to a low state when the input voltage or AVDD is below a specified value. This prevents the main MOSFET switch from being turned on during a power-up or power-down sequence. The MCP1631/MCP1631V provides a thermal shutdown protection feature. If the internal junction temperature of the device exceeds the overtemperature setpoint limit, the overtemperature protection feature will pull the V_EXT output low to turn off the MOSFET switch.

SEPIC TOPOLOGY

We shall examine the SEPIC topology and demonstrate the capability of the MCP1631 family in current control applications using the SEPIC topology.

The SEPIC Topology (Single Ended Primary Inductive Converter) follows the flyback design, adding a coupling capacitor between the two windings of a transformer.

The output voltage may be lower or higher than the input voltage, resulting in buck or boost operation.

This topology can use two separate inductors or a single transformer with two coupled windings. The coupled inductor solution saves PCB space and reduces radiated EMI. Another big advantage of using coupled inductors is the fact that only half of the calculated inductance needs to be used if the inductors are coupled. A capacitor connected between the primary inductor and secondary inductor offers DC isolation and protection against a shorted load. The capacitor clamps the winding leakage inductance energy and eliminates the need for a snubber circuit. The inductor input smooths the current draw and reduces the required input filtering.

A single low-side switch reduces MOSFET drive voltage requirements and current limit protection complexity. The switch current can be sensed using a ground referenced sense resistor connected to the MOSFET source pin.

A typical SEPIC converter design that is used with the MCP1631HV PWM controller is shown in Figure 8.

We can visualize the waveforms for a better understanding of how the SEPIC works. We will consider a coupled inductor SEPIC operating in Continuous Current (CC) mode and refer to Figure 9 for this example. L1 and L2 are wound on the same core. The SEPIC switch (Q1) is turned on at the start of the cycle. The current in the primary winding L1 (I_L1) will ramp up at a rate of V_IN / L1. The current in the secondary winding L2 (I_L2) ramps at V_CC/L2.

The DC voltage across the isolation capacitor C_C (V_CC) is equal to V_IN. The switch current (Q1) is equal to the sum of I_L1 and I_L2 during the switch on time. The input current through L1 is continuous, minimizing the input ripple current compared to other topologies.

![FIGURE 8: SEPIC Topology.](image)

When Q1 turns off, the stored energy in L1 forces the current to continue flowing through the coupling cap (C_C) and the rectifier (D1) to the output. A second current flow exists due to L2 releasing its stored energy. Current flows from L2 through the rectifier (D1) to the output. The total rectifier current is the sum of I_L1 and I_L2 during the switch on time. During the switch-off time, current flowing through the rectifier to the output must also replenish the output capacitor (C_OUT). Looking at the output capacitor current waveform (I_C_C), it can be seen that the output current of the SEPIC converter is supplied by the output capacitor when the switch is on and supplied by the inductors when the switch is off.

The transfer function for the SEPIC converter is derived in a similar fashion as other switching power topologies. The volt-time product of the magnetics must be balanced and the charge-time product on all capacitors must be balanced for each switch period.

The input block components L1 and Q1 act like a standard boost converter input stage. The output block components L2, D and C_OUT act like an inverted buck-boost converter. The goal is to determine the volt-time product on the magnetics during the switch on time and set that equal to the volt-time product on the magnetics during the switch off time.

Basic definitions for design are:

- $$T_{SW} = \text{Switching Period}$$
• \( f_{SW} = \) Switching Frequency
• \( t_{ON} = \) Switch on time
• \( t_{OFF} = \) Switch off time
• Duty Cycle \( D = \frac{t_{ON}}{T_{SW}} = \frac{t_{ON}}{t_{ON} + t_{OFF}} \)

As \( D \) can have values between 0 and 1, the previous equation can be written as: \( 1 - D = \frac{t_{OFF}}{t_{ON} + t_{OFF}} \).

Balance the inductor volt-time product in the boost stage (L1).

For Q1 Turned on (+ Slope):

**EQUATION 6:**

\[
\Delta I_{L1} / t_{ON} = V_{IN} / L_1
\]

For Q1 Turned off (- Slope):

**EQUATION 7:**

\[
\Delta I_{L1} / t_{OFF} = \frac{V_{CC} + V_{OUT} - V_{IN}}{L_1}
\]

The inductor current area defined by the slopes must be equal for volt-time balance.

Solving **Equation 6** and **Equation 7** by extracting \( \Delta I_{L1} \), we will obtain \( V_{CC} \).

**EQUATION 8:**

\[
V_{IN} \times t_{ON} = (V_{CC} + V_{OUT} - V_{IN}) \times t_{OFF}
\]

As mentioned previously, duty cycle \( D = \frac{t_{ON}}{t_{ON} + t_{OFF}} \) or \( (1 - D) = \frac{t_{OFF}}{t_{ON} + t_{OFF}} \). If we multiply both sides of **Equation 8** with \( 1 / (t_{ON} + t_{OFF}) \), we will introduce \( D \) into the following equation:

**EQUATION 9:**

\[
V_{IN} \times D = (V_{CC} + V_{OUT} - V_{IN}) \times (1 - D)
\]

**EQUATION 10:**

\[
V_{CC} = V_{IN} \times \left( \frac{1}{1 - D} \right) - V_{OUT}
\]

The secondary (L2) inductor current area defined by the slopes must also be equal. The volt-time product is solved similar to the primary inductor volt-time product.

For Q1 Turned on (+ Slope):

**EQUATION 11:**

\[
\Delta I_{L2} = \frac{V_{CC}}{t_{ON} \times L_2}
\]

**FIGURE 9:** SEPIC Converter Waveforms.
For Q1 Turned off (- Slope):

**EQUATION 12:**

\[
\frac{\Delta I_2}{t_{\text{OFF}}} = \frac{V_{\text{OUT}}}{L_2}
\]

As above, we'll solve Equation 11 and Equation 12 for \(V_{\text{CC}}\).

Inductor slope's must be equal for volt-time balance, so:

**EQUATION 13:**

\[
t_{\text{ON}} \times V_{\text{CC}} = t_{\text{OFF}} \times V_{\text{OUT}}
\]

As shown above, duty cycle \(D\) can be introduced in Equation 13 by multiplying both sides with \(1 / (t_{\text{ON}} + t_{\text{OFF}})\):

**EQUATION 14:**

\[
V_{\text{CC}} = V_{\text{OUT}} \times \left(\frac{1-D}{D}\right)
\]

Setting Equation 10 equal to Equation 14 yields:

**EQUATION 15:**

\[
V_{\text{CC}} = V_{\text{IN}} \times \left(\frac{1}{1-D}\right) - V_{\text{OUT}} = V_{\text{OUT}} \times \left(\frac{1-D}{D}\right)
\]

The solution for \(V_{\text{OUT}}/V_{\text{IN}}\) is the transfer function of the SEPIC converter in continuous mode:

**EQUATION 16:**

\[
\frac{V_{\text{OUT}}}{V_{\text{IN}}} = \left(\frac{D}{1-D}\right)
\]

By rearranging Equation 16 we get \(V_{\text{IN}} = V_{\text{OUT}} \times (1-D)/D\). Substituting that result into Equation 14 results in:

**EQUATION 17:**

\[
V_{\text{CC}} = V_{\text{IN}}
\]

The voltage across the coupling capacitor (\(C_{\text{C}}\)) is equal to \(V_{\text{IN}}\). This occurs as long as the capacitance of \(C_{\text{OUT}}\) is adequate to supply the output current during the switch ON time and the system operates in continuous current mode.

Until now, the diode forward voltage drop (\(V_{\text{D}}\)) has been ignored at the output because of its low value. If the output voltage is low, the forward voltage drop of the diode may be significant. When the diode drop is considered, the maximum duty cycle will be:

**EQUATION 18:**

\[
D_{\text{max}} = \frac{(V_{\text{OUT}} + V_{\text{D}})/(V_{\text{IN},\text{min}} + V_{\text{OUT}} + V_{\text{D}})}{}
\]

The first step in calculating the inductor current is to know the maximum output power required. An efficiency estimate of 85% for a SEPIC converter can be used to approximate the input current. The average input current is equal to the input power divided by the input voltage.

**EQUATION 19:**

\[
P_{\text{OUT}} = V_{\text{OUT}} \times I_{\text{OUT}}
\]

**EQUATION 20:**

\[
P_{\text{IN}} = P_{\text{OUT}}/\text{(Efficiency)}
\]

**EQUATION 21:**

\[
I_{\text{IN,Avg}} = P_{\text{IN}}/V_{\text{IN}}
\]

The inductor value is calculated by:

**EQUATION 22:**

\[
L_1 = L_2 = \left(\frac{V_{\text{IN},\text{min}} \times D_{\text{max}}}{(\Delta I_L \times f_{\text{SW}})}\right)
\]

\(\Delta I_L\) is the desired inductor peak to peak ripple current. A good \(\Delta I_L\) approximation is 20% of the output current. The approximate ripple current for a 1A output current would be 200 mA.

If \(L_1\) and \(L_2\) are wound on the same core, which is the case of this SEPIC design, the value of inductance in the equation above is replaced by \((1/2)\) due to mutual inductance.

**EQUATION 23:**

\[
\frac{L}{2} = \left(\frac{V_{\text{IN},\text{min}} \times D_{\text{max}}}{(2(\Delta I_L \times f_{\text{SW}}))}\right)
\]

The maximum inductor current (ripple and peaks for \(L_1\) and \(L_2\)) can be calculated to avoid saturating the inductor.

**EQUATION 24:**

\[
\Delta I_{L,\text{ON}} = \left(\frac{V_{\text{IN}} \times t_{\text{ON}}}{(L/2)}\right)
\]
The coupled inductor winding currents calculated above are used to determine the wire size and core of the inductor. High switching frequency has several advantages: smaller ripple current, lower peak and RMS current and lower volt-time product on the inductor core. This leads to smaller and lower-cost solutions.

The switch current (IQ1) is equal to the combination of the winding currents during the switch on time. When the switch is turned on, it conducts the current from L1 and L2.

The peak Q1 switch voltage is equal to \( V_{IN} + V_{OUT} \) for the SEPIC converter. Any leakage inductance voltage spike is clamped through the output diode by the output capacitor. A switch voltage rating for applications should be at least:

The maximum reverse voltage across the SEPIC diode occurs during the switch ON time. The cathode of the schottky diode is connected to \( V_{OUT} \), the anode of the schottky diode is connected to the SEPIC coupling capacitor. The voltage across the coupling capacitor voltage is equal to \( V_{IN} \). When Q1 is ON, the voltage across the diode is:

A schottky diode is recommended for low voltage applications because of the low forward voltage drop. The lower forward drop improves converter efficiency.

The selection of the SEPIC capacitor, \( C_c \), depends on the RMS current, which is passing through it. The RMS current in the SEPIC coupling capacitor is mainly dependant upon output power with some influence by inductor ripple current. The RMS current in \( C_c \) can be calculated by solving Equation 31 for both states of switching, \( t_{ON} \) and \( t_{OFF} \). Equation 32 is the RMS expression of a trapezoid waveform.

For the SEPIC converter, the coupling capacitor voltage is approximated as a DC value when deriving the duty cycle. The ripple voltage should be no more than a few percent (5% in practice) of the voltage across the capacitor or the input voltage. The SEPIC capacitor must be rated for a large RMS current in relation to the output power. As the output power increases, the capacitor ripple current will increase as well. This makes the SEPIC converter better suited for lower power applications where the RMS current through the capacitor is relatively small. The worst case RMS current in the coupling capacitor will occur at maximum output power and minimum input voltage.

The same algorithm should to be applied to the output capacitor. Ripple voltage (\( \Delta V \) or \( V_{ripple} \)) should be no more than a few percent of the voltage across the capacitor. When the switch is on \( (t_{ON} = D / f_{SW}) \), the inductor L1 is charging and the output current is supplied by the output capacitor \( C_{OUT} \).
EQUATION 35:

\[ C_{OUT} = \left( \frac{I_{OUT}}{V_{ripple}} \right) \times \left( \frac{D}{f_{SW}} \right) \]

The input capacitor \( C_{IN} \) should be capable of handling the input RMS current. The input current waveform is continuous and triangular. The inductive input ensures that the input capacitor sees low ripple currents from the power supply. The input capacitor provides a low impedance source for the SEPIC converter in cases where the power source is not immediately adjacent to the SEPIC powertrain.

MCP1631HV PROGRAMMED CURRENT SOURCE REFERENCE DESIGN FOR DIMMING POWER LEDS

Microchip has developed a reference design for a dimmable power LED lighting application to demonstrate the capability of the MCP1631HV PWM Controller.

<table>
<thead>
<tr>
<th>Programmed output current (Note 1)</th>
<th>Dimming ratio</th>
<th>Max. Output power (Note 1)</th>
<th>Over voltage protection</th>
<th>Thermal shutdown (Note 2)</th>
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<tr>
<td>700 mA</td>
<td>10:1</td>
<td>8.5W</td>
<td>9V</td>
<td>40°C</td>
</tr>
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</table>

Note 1: Maximum programmed output current in the source code is 1A. Total power output should not exceed 8.5W.

2: This option is not enabled in default compiled .hex file available on the Microchip website.

The reference board is capable of driving 1A maximum @ 8.5W output power. Efficiency is typical 85% at maximum power. The default parameters are displayed in Table 1.

An input terminal block is provided for connecting the input source voltage to the board. An output header is also provided for attaching the load LED and an external 10k thermistor. The thermistor should be mounted on the LED heat sink near the LED. The temperature monitoring feature must be enabled in the source code before compiling. The default value for overtemperature shutdown is 40°C. Temperature monitoring is not enabled in the default .hex file. The recommended thermistor is EPCOS Inc. PN B57703M0103G040.

A Microchip ICD2 / PICkit 2 compatible programming header is available for updating the reference board firmware contained within the PIC16F616. The PIC16F616 microcontroller is used to regulate output voltage, current, switching frequency and maximum duty cycle. The MCP1631HV generates the required switch duty cycle and provides fast overcurrent protection based upon the various external inputs. External signals include the reference oscillator, the reference voltage, the feedback voltage and the current sense. The output drive signal is a square-wave pulse used to drive the gate of the powertrain MOSFET switch. The power train topology used for the MCP1631HV Programmable Current Source is a Single-Ended Primary Inductive Converter (SEPIC).

The MCP1631HV Programmable Current Source has the following features:

- Input Operating Voltage Range: +3.5V to +16V
- Maximum output current 700 mA with dimming ratio 10:1 (70 mA/step for pre-compiled .hex file)
- Drive one or more power LEDs. Limited by OVP and 8.5W power capability
- Optional battery charger software on Microchip website for charging Li-Ion, NiMH, and NiCd battery packs. Charge 1 or 2 single cell Li-Ion batteries in series or 1 to 4 single cell NiMH or NiCd batteries in series
- Maximum output capability for all modes of operation: 1A @ 8.5W
- ON/OFF/Dimming switch
- Status Indication
- Hardware and software overvoltage shutdown set at 9.0V
FIGURE 10: Output Current Regulation vs. Input Voltage in Driving LED Mode.

Operation
The MCP1631HV Digitally Controlled Programmable Current Source Reference Design has a red/green dual color LED (D2) to indicate status and faults. The green LED will flash with a one second period when the system is activated and current is being supplied to the load.

Press button S1 to enable the output and drive the LED with the minimum programmed current of 70 mA. Each additional press of S1 will increment the drive current by 10% of the final programmed value. Press and hold S1 for at least 2 seconds until the red LED indicator is activated to switch the output current off. If a fault condition such as no load, overvoltage, or overtemperature is present when attempting to turn the output on, the system will stay off and the fault condition will be indicated by the flashing red indicator LED. The faults are coded as following:

- Red LED flashes at 1 Hz rate if an overtemperature of the LED case has been detected. The fault is Thermal Shutdown
- Red LED flashes at 2 Hz rate if an overvoltage or a missing/open load has been detected. The fault is Overvoltage Shutdown

An overvoltage/overtemperature fault condition will be verified 5 times by the microcontroller firmware before the fault is validated and the system shuts down.

Two parallel resistors, R_{10} and R_{11}, are used to sense the MOSFET switch (Q_1) current (Figure 11). The current sense signal is applied to the CS comparator input of MCP1631HV. The load current is equal to the current in the secondary inductor L_1:B. Sense resistors R_2 and R_3 are used to sense the load current. The load sense signal is amplified by the IS_{in} current sense amplifier before entering the feedback pin (FB). A reference voltage signal from the PIC16F616 supplies the V_{\text{REF}} current reference voltage for the MCP1631HV. The pulse width modulated V_{\text{EXT}} signal that drives the MOSFET switch is generated by the MCP1631HV using the microcontroller current reference PWM signal and the load current feedback signal. The current feedback signal is integrated with the current reference signal. The integrated signal compared with the switch current sense and an artificial compensation ramp to modulate the switch drive signal. The artificial compensation ramp is generated from the PIC16F616 OSC output signal to prevent the system from switching between continuous and discontinuous operation. The bi-modal operation would cause the duty cycle to vary when D > 50%. The artificial ramp voltage supplements the switch current sense voltage to guarantee the CS signal rises above the reference signal before the end of the integrator period. When the CS signal rises above the reference signal, the MOSFET switch will be turned off, resetting the integrator. The artificial ramp slope for the SEPIC will be the same as the slope of the current during the OFF time of the oscillator. The artificial ramp circuit block consists of C_{10}, R_9, R_{13}, and Q_2 which is driven with the OSC signal.

A resistor divider network composed of R_5, R_8, and R_{15} is used for load voltage sensing. R_5 senses overvoltage conditions and feeds directly into the MCP1631HV overvoltage input pin. R_8 senses the load voltage. The load voltage tap at R_8 is connected to the MCP1631HV internal voltage follower amplifier via the V_{SIN} pin. The corresponding V_{SOUT} signal is fed to the PIC16F616 A/D RA2 port.

When a thermistor is used, it is placed across J2-4 and J2-5. The thermistor forms a voltage divider with R_1, a 10 k\Omega resistor. The voltage across the thermistor is fed to the A/D port RA4 of the PIC16F616. If an overtemperature condition is detected, the microcontroller will disable the PWM signal of MCP1631.

The OSC signal is generated directly by the PIC16F616 PWM hardware at port RC5.

MCP1631HV Digitally Controlled Programmable Current Source Reference Design Firmware
The source code for Microchip’s MCP1631HV Digitally Controlled Programmable Current Source Reference Design was developed using mikroElektronica’s mikroC Compiler for PIC Processors. The software has conditional compiler options for user selectable parameters and function. The LED driver is the default compiler option. The NiMH/NiCd and Li-Ion battery charger options are selectable at compile time by modifying a parameter in the header file. The mikroC compiler may be downloaded at: http://www.mikroe.com.

Please consult the mikroC website and compiler Help for more information about the compiler.
FIGURE 11: MCP1631HV Programmable LED Current Source Reference Design Schematic.
The reference board PIC16F616 microcontroller has been pre-programmed with the LED driver option. The reference board may also be operated as a battery charger. The user may re-configure and compile the source code for Li-Ion or NiMH/NiCd battery charging instead of LED driving.

The firmware packet has several files for LED dimming and battery charging.

- **LED** = 00234R1.hex
- **Li-Ion** = 00234R1-Lion_Charger.hex
- **NiMH** = 00234R1-NiMH_NiCd_Charger.hex
- **NiCd** = 00234R1-NiMH_Nicd_Charger.hex

The C source code file is 00234R1.c and the header file is 00234R1.h. These two files are referenced by the mikroC project file: 00234R1.ppc.

To setup the parameters, the user starts with the 00234R1.h file. Set the function of interest for the reference board as enabled. The example below is for an LED driver.

```c
/* Enable support for Lithium Ion batteries */
#define LION_SUPPORT DISABLED

/* Enable support for Nickel Metal Hydride batteries */
#define NIMH_SUPPORT DISABLED

/* Enable support for LED Driver */
#define LED_DRIVER_SUPPORT ENABLED
```

The charging/driving current profile parameters are set in separate sections of the header file for the project. The LED driver overvoltage value is set to 9V by parameter LED_DRIVER_OVER_VOLTAGE. The LED driver starting current value is set to 70 mA by parameter LED_DRIVER_CONDITION_CURRENT. The LED maximum current value is set to 700 mA by parameter LED_DRIVER_CURRENT. To evaluate the board, a Cree XREWHT-L1-0000-009E7 with warm white LEDs that can be driven at 700 mA maximum should be used. The overtemperature setpoint is enabled by setting OVERTEMPERATURE_SUPPORT to ENABLED. The default overtemperature set point is +40°C.

A 10k NTC thermistor must be connected between J2-4 and J2-5 of the J2 connector when overtemperature support is enabled. The thermistor should be placed as close as possible to the LED mounting area. The number of LEDs value is set to 1 by parameter NUMBER_OF_CELLS_DEFAULT. These default values are for a typical 3W power LED.

```c
#define LED_DRIVER_OVER_VOLTAGE ((int)(9000.0/BATTERY_MV_PER_BIT))
#define LED_DRIVER_CONDITION_CURRENT ((int)(70.0/BATTERY_MA_PER_BIT))
#define LED_DRIVER_CURRENT ((int)(700.0/BATTERY_MA_PER_BIT))
#define LED_DRIVER_OVTEMP_SETPOINT ((float)(THERMISTOR_OHMS_40C))
#define OVERTEMPERATURE_SUPPORT DISABLED
#define NUMBER_OF_CELLS_DEFAULT 1
```

The charging/driving current profile parameters are set in separate sections of the header file for the project. The LED driver overvoltage value is set to 9V by parameter LED_DRIVER_OVER_VOLTAGE. The LED driver starting current value is set to 70 mA by parameter LED_DRIVER_CONDITION_CURRENT. The LED maximum current value is set to 700 mA by parameter LED_DRIVER_CURRENT. To evaluate the board, a Cree XREWHT-L1-0000-009E7 with warm white LEDs that can be driven at 700 mA maximum should be used. The overtemperature setpoint is enabled by setting OVERTEMPERATURE_SUPPORT to ENABLED. The default overtemperature set point is +40°C.

A 10k NTC thermistor must be connected between J2-4 and J2-5 of the J2 connector when overtemperature support is enabled. The thermistor should be placed as close as possible to the LED mounting area. The number of LEDs value is set to 1 by parameter NUMBER_OF_CELLS_DEFAULT. These default values are for a typical 3W power LED.
The OSC reference signal supplied to the MCP1631HV PWM controller is set by the PIC16F616. The frequency may be set to 200 kHz or 500 kHz. The parameters are set in the main C file 00234R1.c.

```c
/* define Oscillator Output PWM frequency Prescale Value for PR2 */
#define OSC_500KHZ_PR2_REG 0x03
#define OSC_200KHZ_PR2_REG 0x09
#define OSC_PR2_REG OSC_500KHZ_PR2_REG
```

The oscillator duty cycle is set to 25% if the SEPIC reference board must work in buck-boost mode. The 25% duty cycle allows the system to boost the output voltage. The parameter is in the 00234R1.h header file.

```c
/* charge oscillator duty cycle */
#define OSCILLATOR_DUTY_CYCLE 25
```

The number of incremental LED current steps is set to 10 by the parameter IREF_INCREMENTS. Each S1 button press will increment the output current by a factor of \( \left( \frac{1}{\text{IREF_INCREMENTS}} \right) \) times the programmed current value up to the programmed current value.

```c
/* define Reference Current Increments when ramping up current at startup */
/* rate is 1 increment every second in Charger Mode or every button push for LED Driver */
#define IREF_INCREMENTS 10
```

Example:
The MCP1631RD-DCPC1 Reference Board needs to drive an LED string composed of 2 white LEDs in series topology with a 350 mA maximum forward current. The board is powered from a 12V source.

Solution:
The system will need to provide 1 LED x 350 mA = 350 mA of drive current, because the LEDs are connected in series topology.

- **IREF_INCREMENTS** is set to 10 to allow 10 incremental current steps.
- **LED_DRIVER_CONDITION_CURRENT** is set to the startup current value of 35 mA.
- **LED_DRIVER_CURRENT** is set to the final current of 350 mA.
- **LED_DRIVER_OVER_VOLTAGE** is set to 8V which is the sum of maximum forward voltages of the two white LEDs.

The system will only need to operate in buck mode because the 12V power supply voltage is greater than the 8V load voltage.

- **OSCILATOR_DUTY_CYCLE** is set to 50% for buck mode.
- **OVERTEMPERATURE_SUPPORT** is set to **DISABLED** because cooling the LED string is not an issue.

The flowchart of the default source code is given in Figure 15. When powered on, the microcontroller initializes the ports and the default system parameters. If push-button S1 is pressed, the load is supplied with condition current. Each additional press of S1 will increase the current until the maximum programmed current is reached. Further presses will not increase the current beyond the maximum value. When S1 is pressed and held for more than 2 seconds, the red LED will turn on and the output will turn off.
FIGURE 15: Firmware Flow Diagram for LED Driver With Dimming.
CONCLUSION

For applications that require intelligent power management solutions like LED drivers, the combination of a microcontroller and the MCP1631 high-speed PWM is very powerful. It brings the programmability benefits of the microcontroller and adds the performance of a high speed analog PWM. The analog PWM will respond to changes in input voltage and output current very quickly. The microcontroller is used for programmability, for establishing LED current, for monitoring the circuit for fault conditions, and for taking the appropriate action in the event of a specific fault.

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